

Turis Schematic STR (FT4)

2016-08-30
REV : A00

DY : None Installed
UMA: UMA only installed
PX: DISCRTE PowerExpress installed

Vegas AMD



Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

Cover Page

Size
A3

Document Number

Turis STR (FT4)

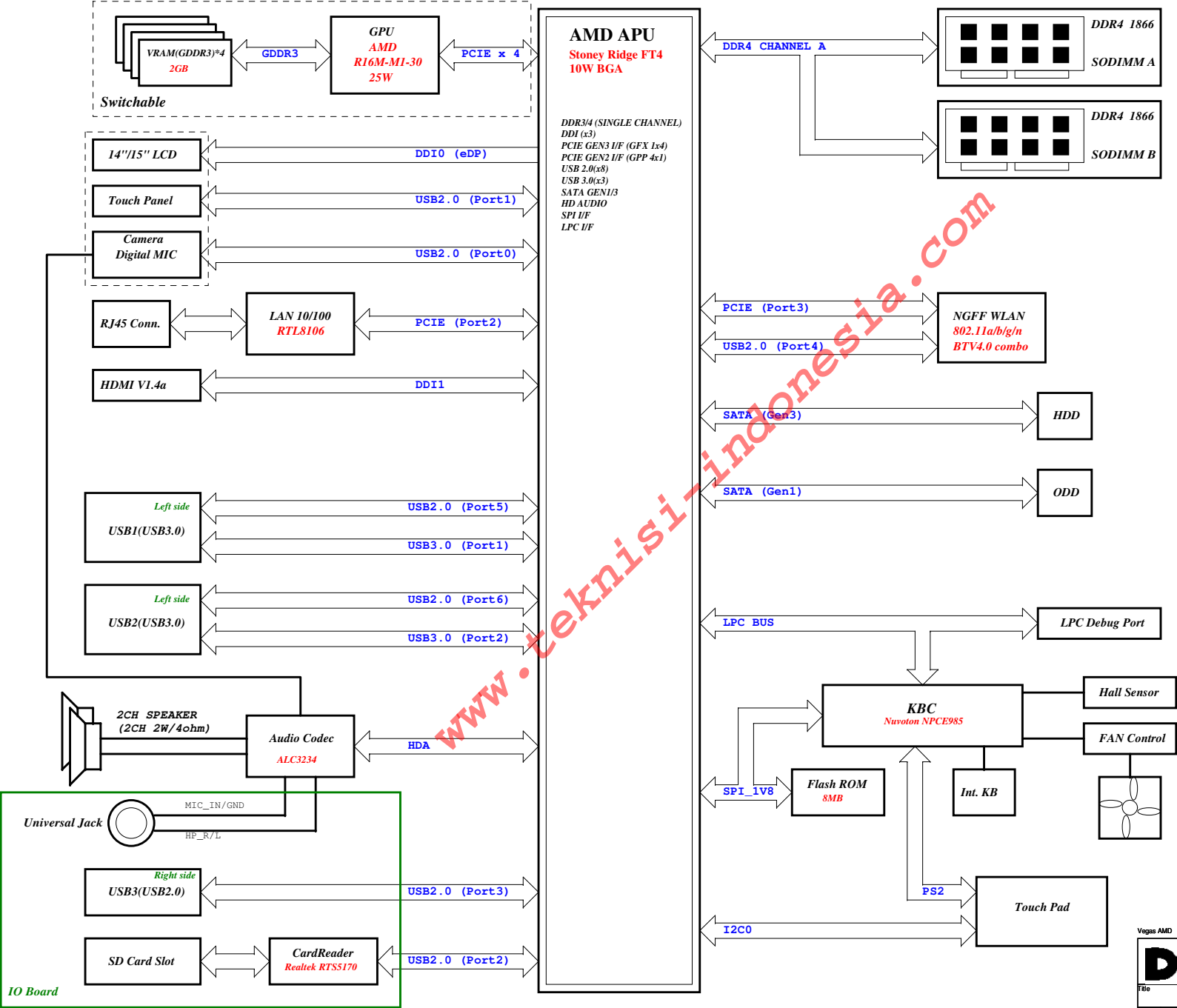
Rev

A00

Date: Monday, August 29, 2016

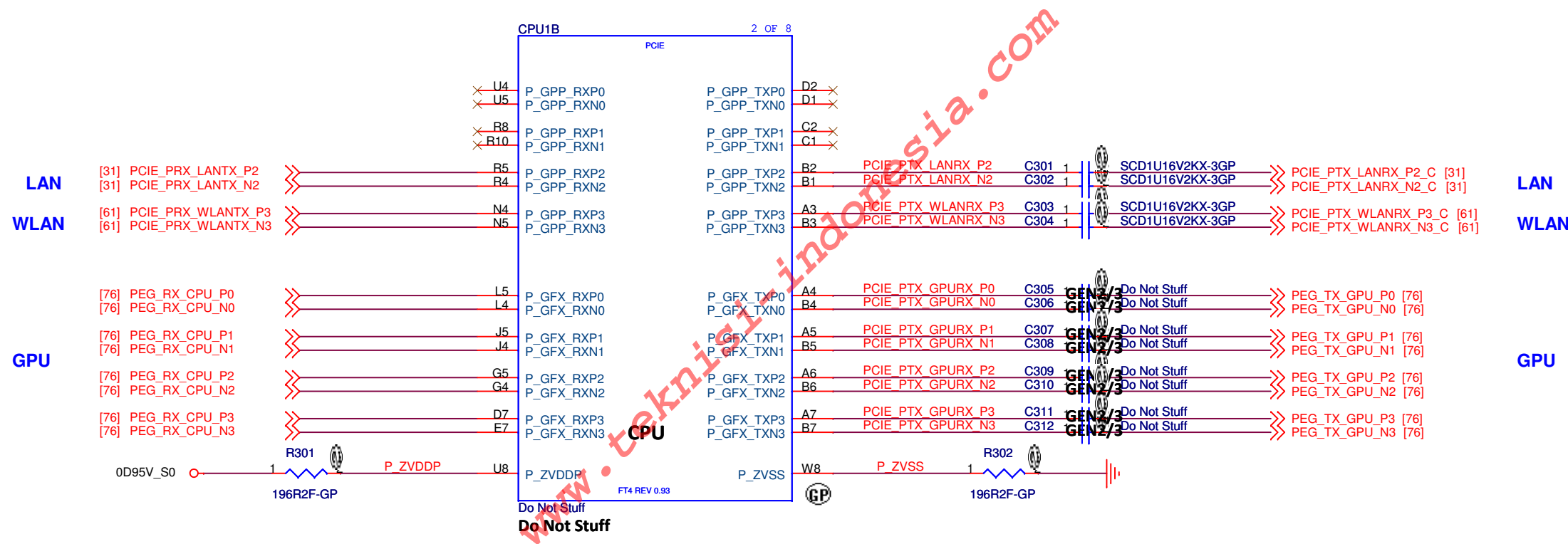
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Turis Stoney Ridge (FT4) Block Diagram




CHARGER		44
ISL88739HRZ-T		
INPUTS	OUTPUTS	
AD+	DCBATOUT	
BT+		
SYSTEM DC/DC		45
TPS51225RUKR		
INPUTS	OUTPUTS	
DCBATOUT	3D3V_AUX_S5 5V_AUX_S5 5V_S5 3D3V_S5	
CPU Core Power		46, 47
ISL62771HRTZ		
INPUTS	OUTPUTS	
DCBATOUT	1V_CPU_CORE 1D2V_CPU_VDDNB	
DDR4 SUS		53
APW8861QBI-TRG		
INPUTS	OUTPUTS	
DCBATOUT	1D2V_S3 0D6V_VREF_S0	
CPU VDDP		52
APW8861QBI-TRG		
INPUTS	OUTPUTS	
DCBATOUT	0D95V_S5	
CPU VDD18		54
RT8068AZQWID		
INPUTS	OUTPUTS	
3D3V_S5	1D8V_S5	
CPU VDDCRGFX		49
APL5337KAI		
INPUTS	OUTPUTS	
1D8V_S5	0D775V_S5	
Switches		40
INPUTS	OUTPUTS	
5V_S5	5V_S0	
3D3V_S5	3D3V_S0	
1D8V_S5	1D8V_S0	
0D95V_S5	0D95V_S0	
PCB LAYER		
L1: TOP L2: VCC L3: Signal L4: Signal L5: GND L6: BOT		

GPP CLK port	Device	CLKREQ#
2	LAN	2
3	WLAN	3
G	GPU	G



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CPU (PCIE)

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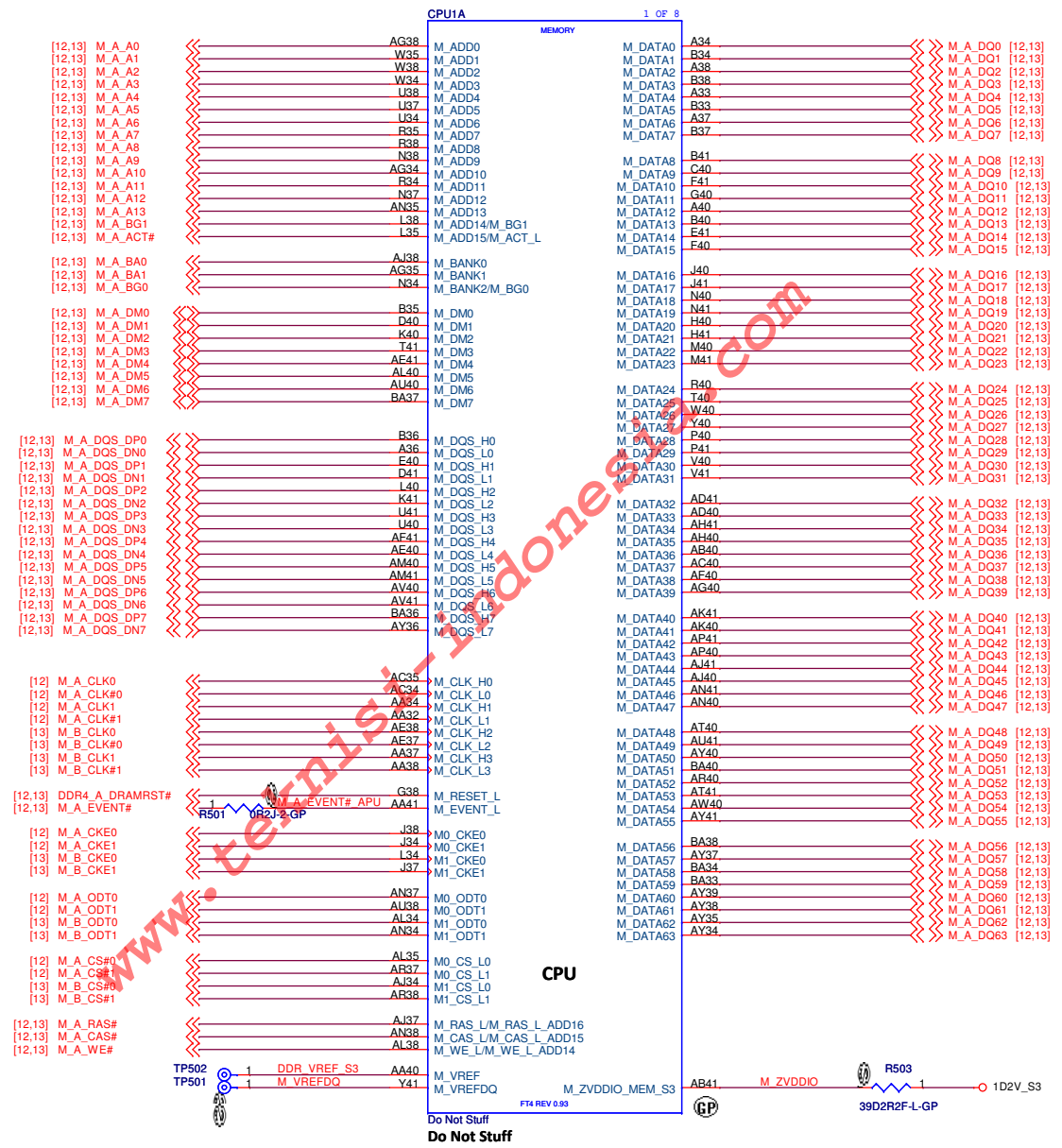
(Blanking)

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1D2V_S3
R504 1KR2J-1-GP M_A_EVENT# APU
AMD Confirm: PU Needed even if not used



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Title

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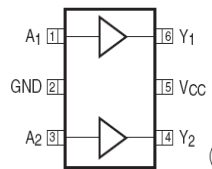
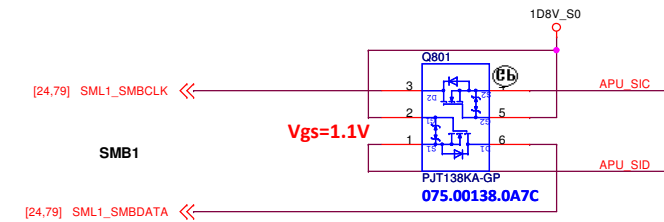
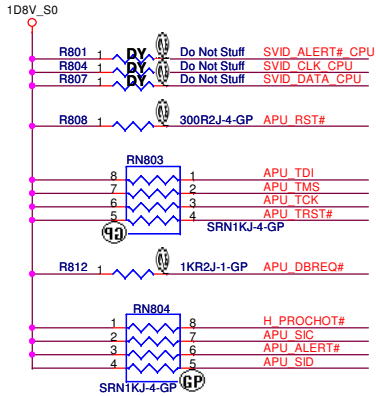
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Pre-PWROK METAL VID CODES

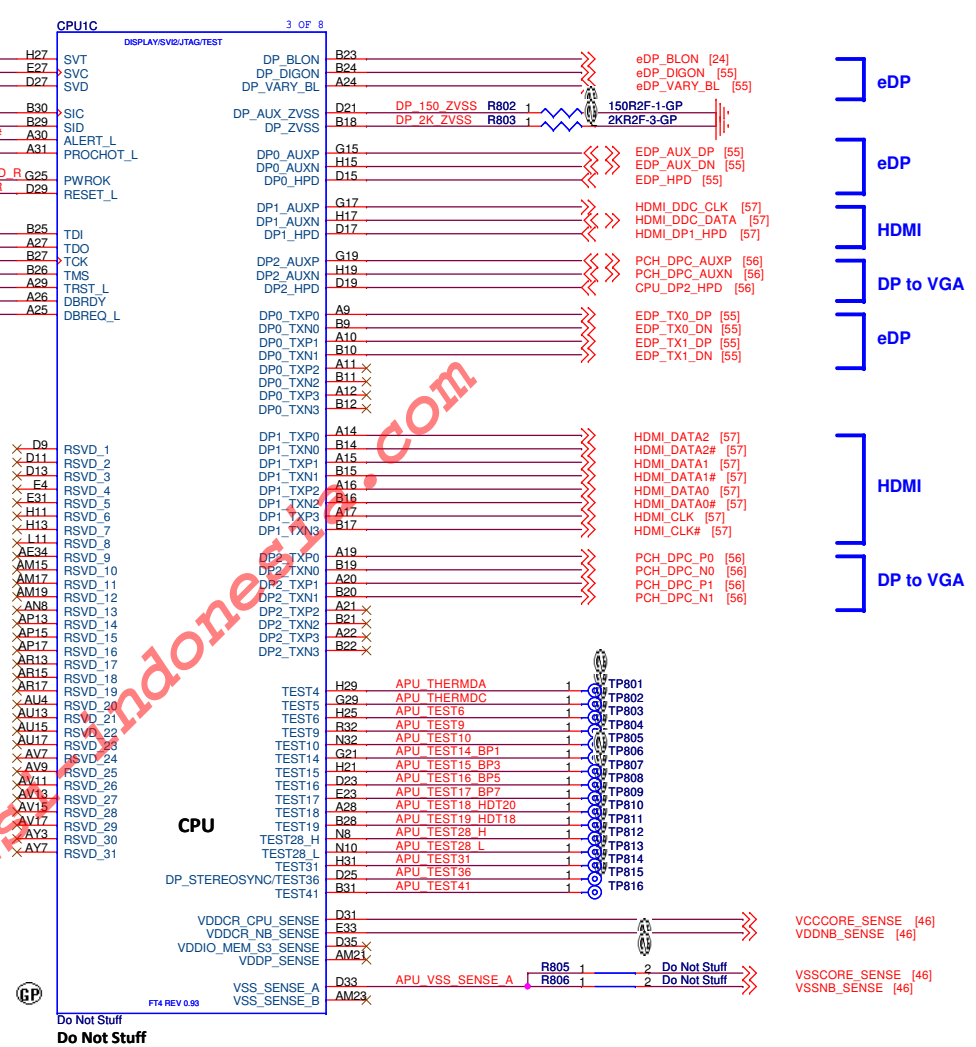
SVD	SVC	Output Voltage
0	0	1.1
1	0	1.0
0	1	0.9
1	1	0.8



HDT+ Connectors

APU core Control

APU_RST_L_BUF [68]
APU_PWROK_HDT [68]



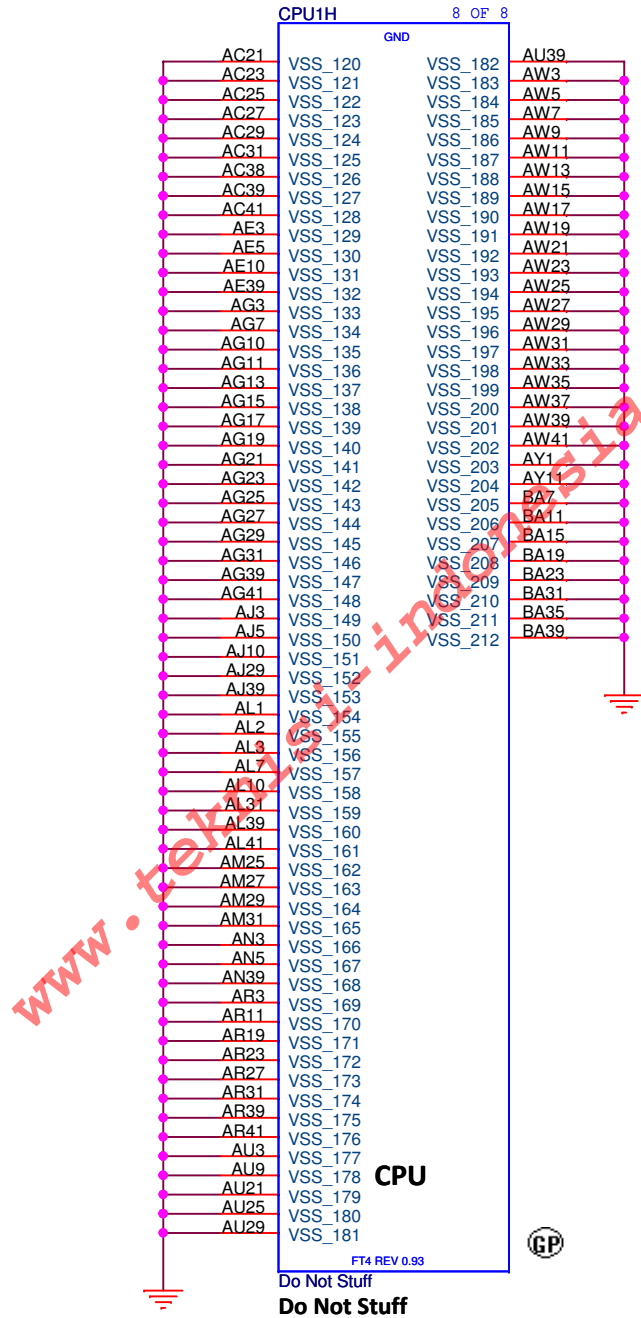
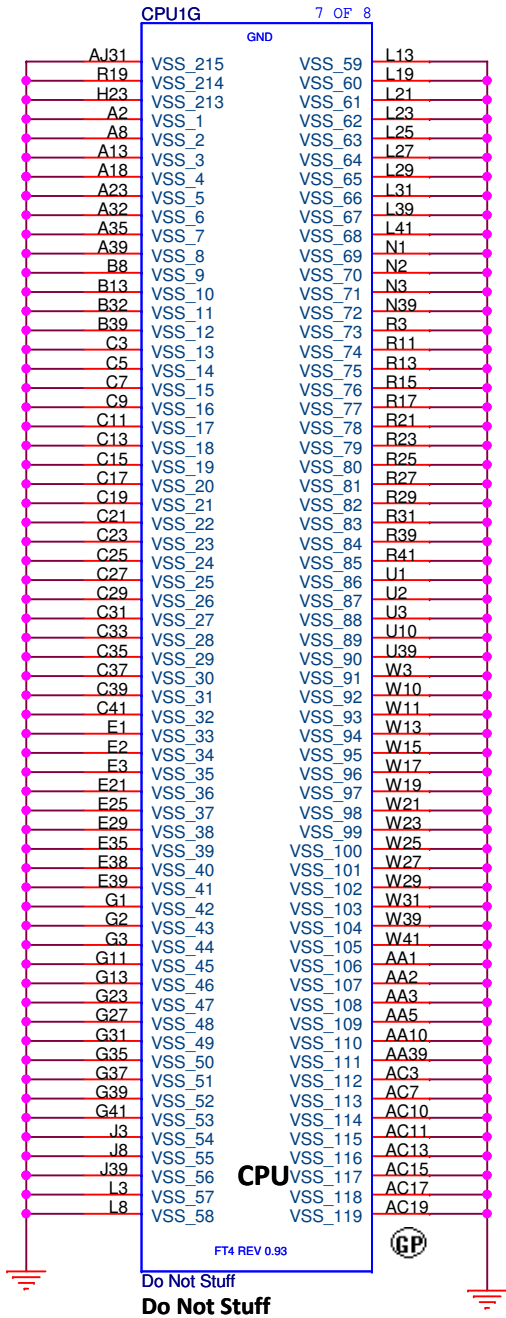
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Title **CPU (DISPLAY/SVI2/JTAG/TES)**

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Title **CPU (VSS)**


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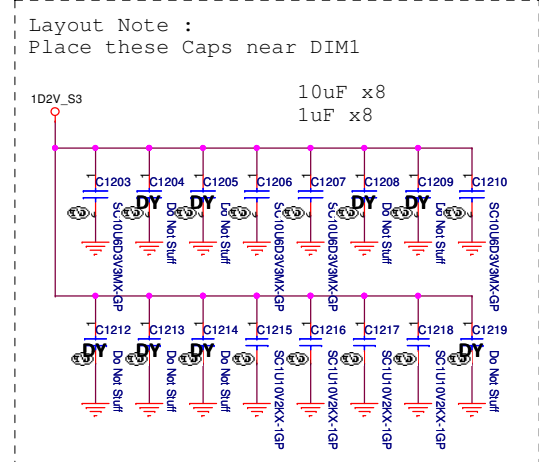
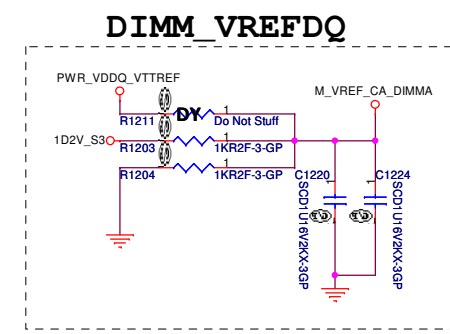
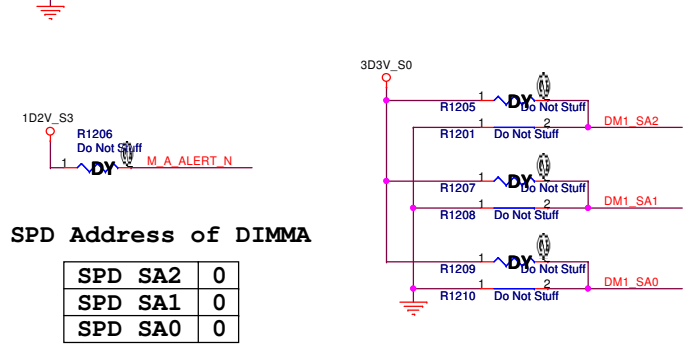
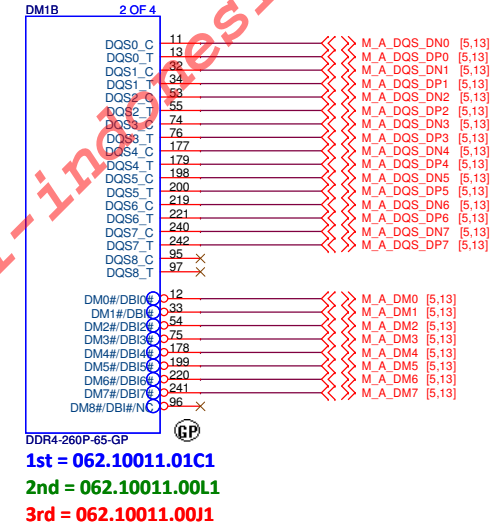
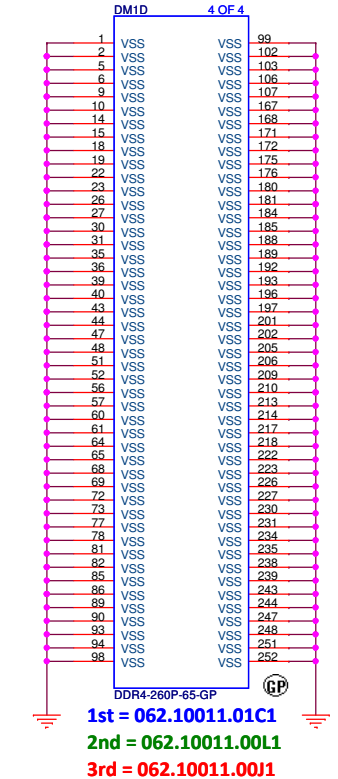
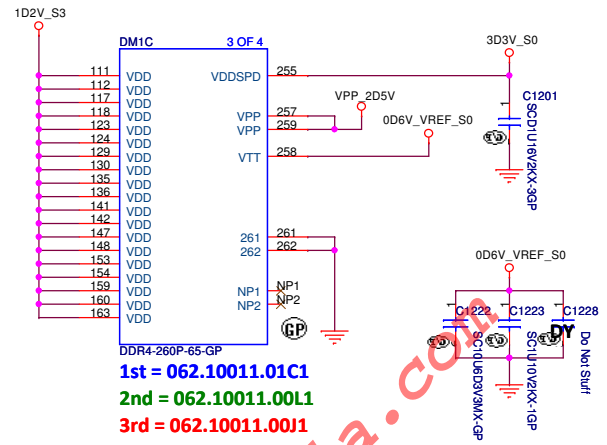
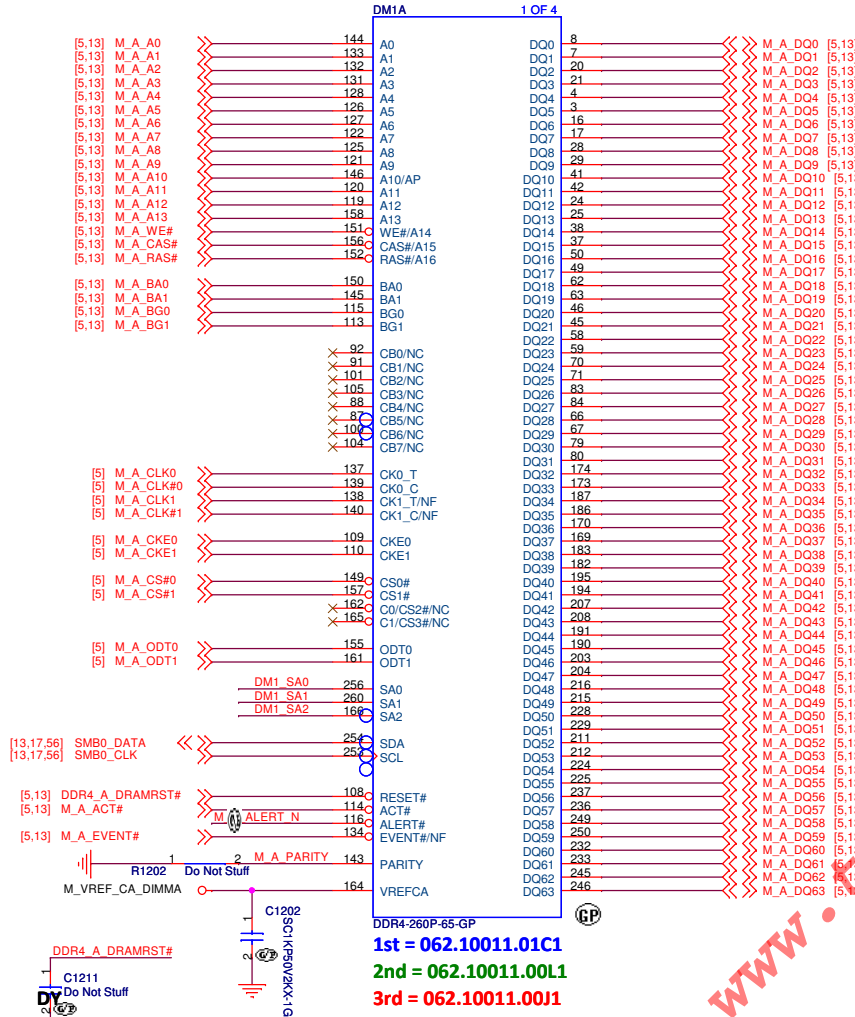
(Blanking)

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SSID = DDR4-SODIMM1



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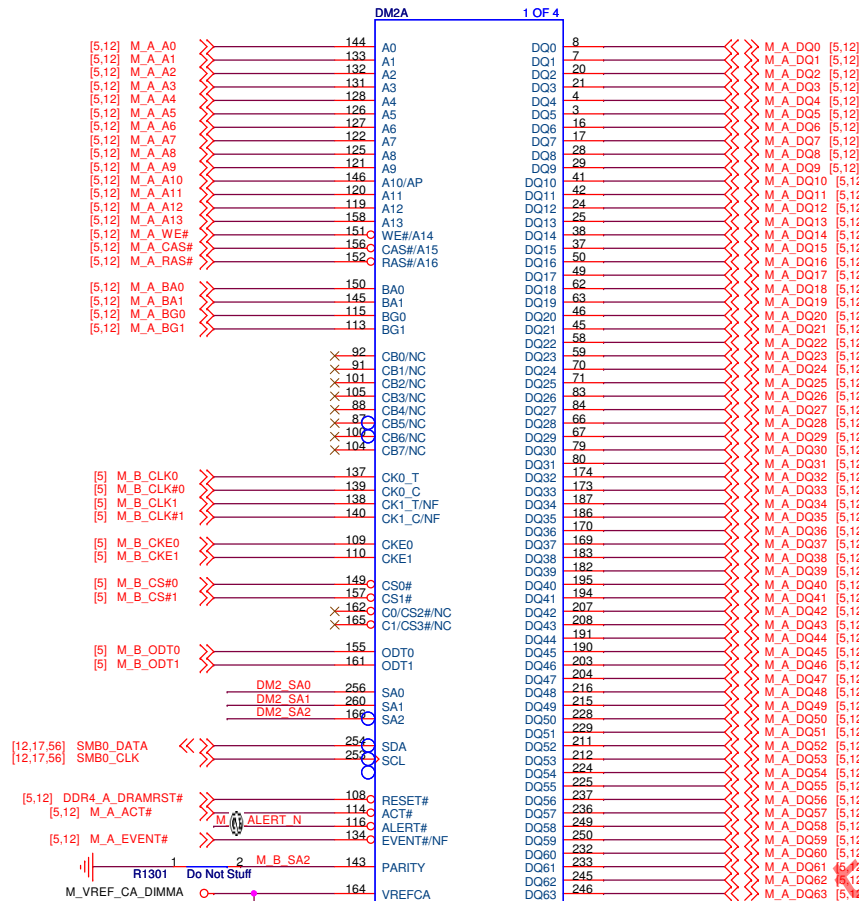
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Title: **DDR4-SODIMM1**

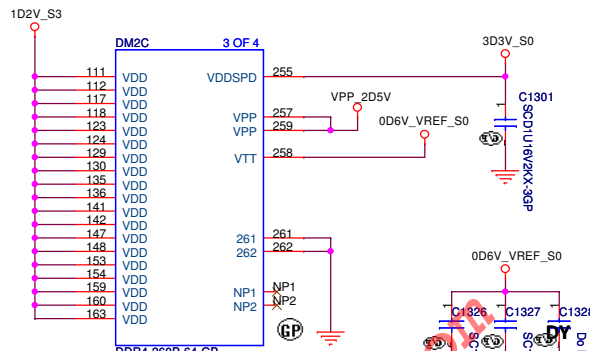
Size A3 Document Number: **Turis STR (FT4)** Rev: **A00**

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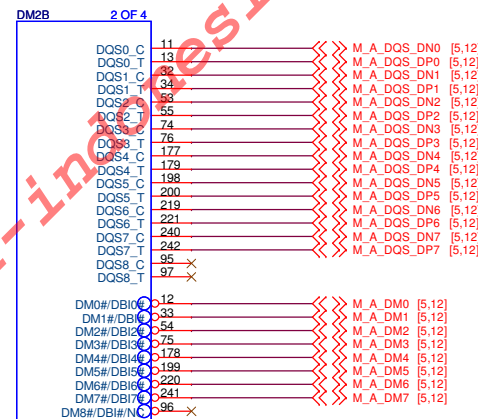
SSID = DDR4-SODIMM2



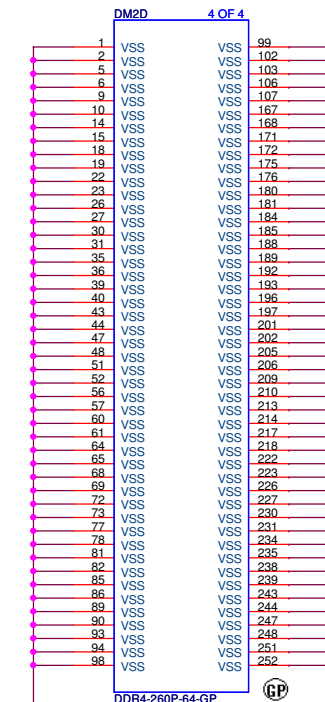
DDR4-260P-64-GP
1st = 062.10011.01B1
2nd = 062.10011.00R1
3rd = 062.10011.00K1
4th = 062.10011.00I1



DDR4-260P-64-GP
1st = 062.10011.01B1
2nd = 062.10011.00R1
3rd = 062.10011.00K1
4th = 062.10011.00I1

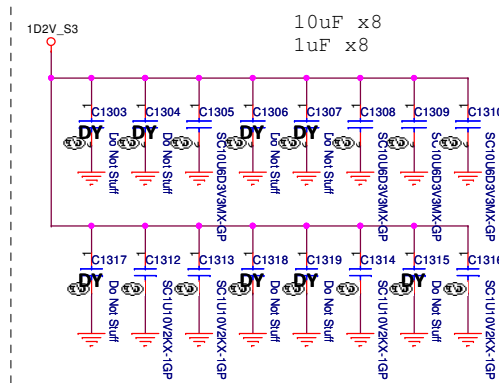


DDR4-260P-64-GP
1st = 062.10011.01B1
2nd = 062.10011.00R1
3rd = 062.10011.00K1
4th = 062.10011.00I1



DDR4-260P-64-GP
1st = 062.10011.01B1
2nd = 062.10011.00R1
3rd = 062.10011.00K1
4th = 062.10011.00I1

Layout Note :
 Place these Caps near DIM1



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DDR4-SODIMM2			
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SPD Address of DIMMA

SPD SA2	0
SPD SA1	0
SPD SA0	1

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SATA HDD

ODD

Pair	Device
0	SATA HDD
1	ODD

GPU

LAN

WLAN

GPP CLK port	Device	CLKREQ#
2	LAN	2
3	WLAN	3
G	GPU	G

[60] SATA_TX_CPU_P0
[60] SATA_TX_CPU_N0
[60] SATA_RX_CPU_P0
[60] SATA_RX_CPU_N0
[60] SATA_TX_CPU_P1
[60] SATA_TX_CPU_N1
[60] SATA_RX_CPU_P1
[60] SATA_RX_CPU_N1

0D95V_S0

[64] SATA_LED#_R
[60] HDD_DEVSLP#

R1605 1 Do Not Stuff
R1606 1 Do Not Stuff

TP1604 1

[20] APU_LPCCLK1

[20,24] LPCCLK0
[68] LPCCLK1
[20,24,68] LPC_FRAME#

[24,68] LPC_RST#
[24] LPC_CLKRUN#
[24] ECSCM#_KBC
[24] ECSCM#_KBC
[24] LPC_SERIRQ

R1629 1 Do Not Stuff
R1630 1 Do Not Stuff
R1632 1 Do Not Stuff
R1634 1 Do Not Stuff
R1635 1 Do Not Stuff
R1631 1 Do Not Stuff

CPU1E

CLK/SATA/USB/SPI/LPC

5 OF 8

BA10 SATA_TX0P
AY10 SATA_TX0N
BA12 SATA_RX0N
BA12 SATA_RX0P
AY9 SATA_TX1P
BA9 SATA_TX1N
BA8 SATA_RX1N
AY8 SATA_RX1P
AU11 SATA_ZVSS
AP11 SATA_ZVDDP

AY30 SATA_ACT_L/AGPIO130
AV31 DEVSLP0/EGPIO67
AU31 DEVSLP1/EGPIO70

GFX_CLKP
GFX_CLKN
GPP_CLK0P
GPP_CLK0N
GPP_CLK1P
GPP_CLK1N
GPP_CLK2P
GPP_CLK2N
GPP_CLK3P
GPP_CLK3N

X48M_X1
X48M_X2

X25M_48M_OSC

APU_LPCCLK0
APU_LPCCLK1
APU_LPC_FRAME#
APU_LPC_AD0
APU_LPC_AD1
APU_LPC_AD2
APU_LPC_AD3

APU_LPC_RST#
APU_LPC_CLKRUN#
APU_LPC_PD#
APU_LPC_PME#
APU_LPC_SMI#
APU_LPC_SERIRQ

Do Not Stuff
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CPU

SPI_CLK/ESPI_CLK/EGPIO117
SPL_DQ/ESPI_DAT0/EGPIO121
SPL_DQ/ESPI_DAT1/EGPIO120
SPL_HOLD/ESPI_DAT3/EGPIO133
SPL_WP/ESPI_DAT2/EGPIO122
SPL_CS1_L/EGPIO118
SPL_TPM_CS_L/AGPIO76
ESPI_ALERT_L/LDRQ0_L
ESPI_RESET_L/KBRST_L/AGPIO129
SPL_CS2_L/ESPI_CS_L/EGPIO119

FT4 REV 0.93

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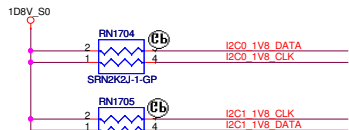
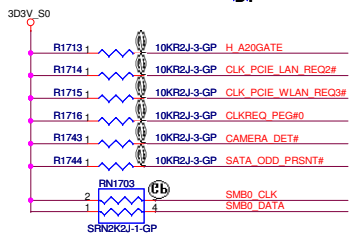
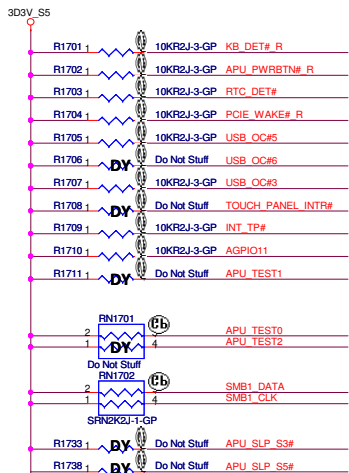
Do Not Stuff

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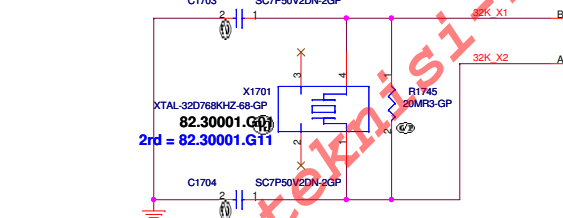
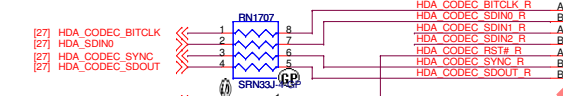
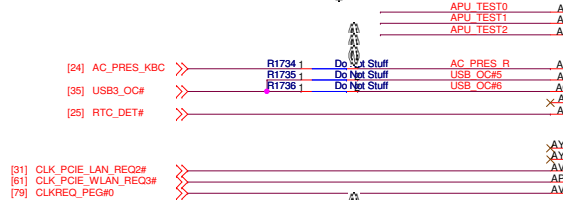
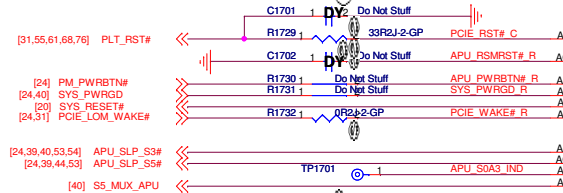
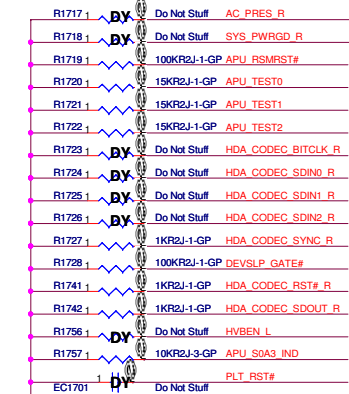
Do Not Stuff

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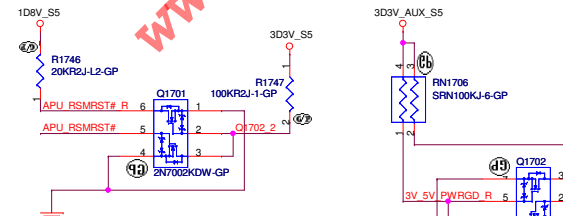
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Pull-up 10Kohm if not used.



Crystal ESR should be between 10Kohm to 65Kohm.



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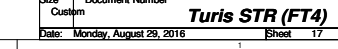
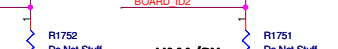
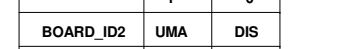
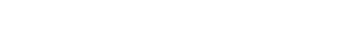
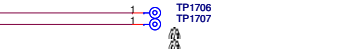
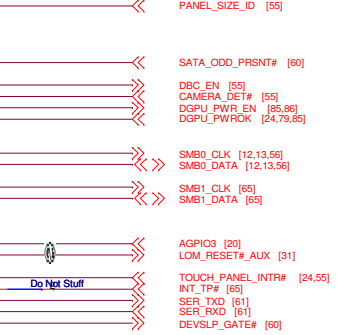
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Do Not Stuff



Vegas AMD

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Title **CPU(ACPI/SD/AZ/GPIO/RTC)**

Size Document Number **Turis STR (FT4)** Rev **A00**

Date: Monday, August 29, 2016 Sheet 17 of 106

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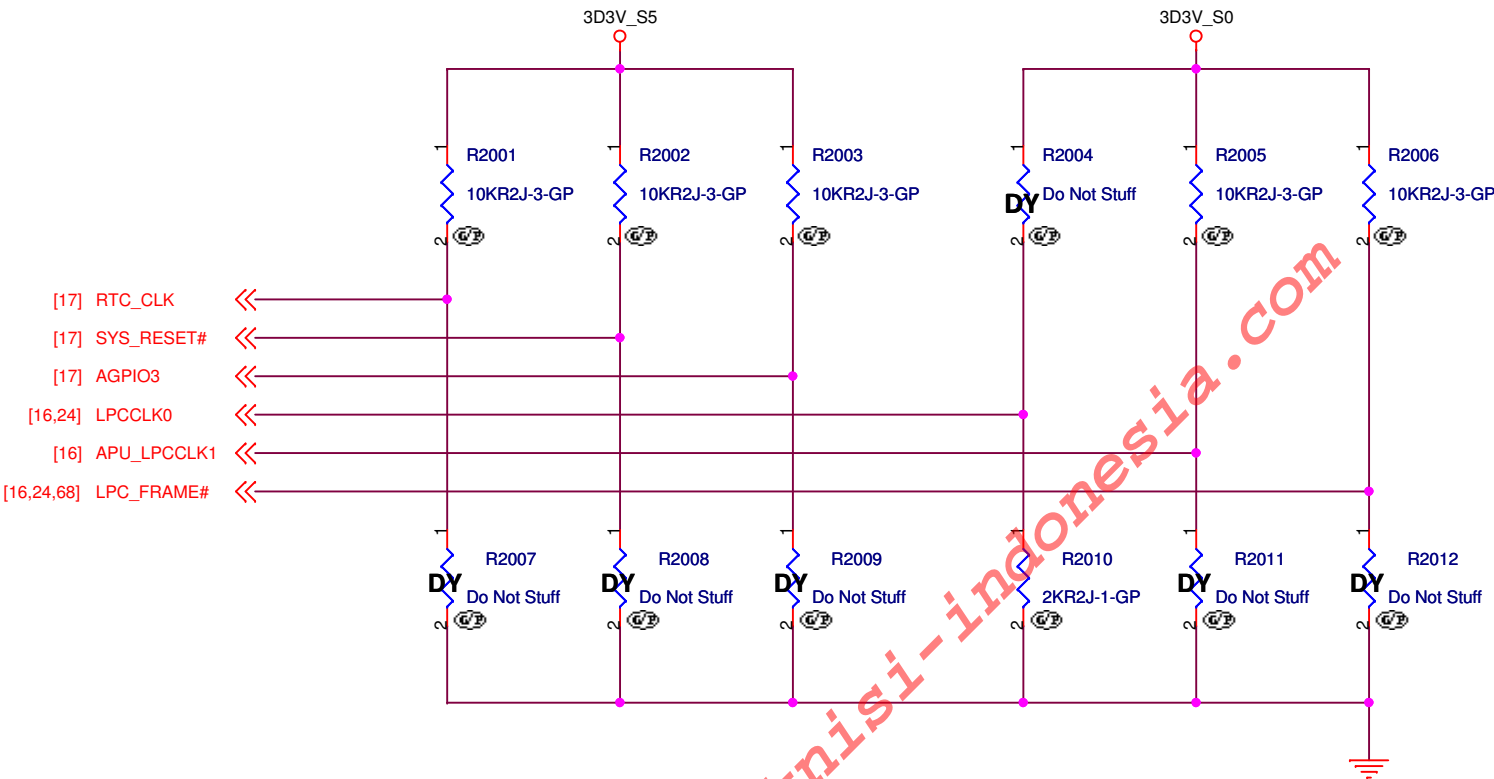
(Blanking)

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SYSTEM STRAPPINGS



REQUIRED SYSTEM STRAPS

	RTC_CLK	SYS_RESET#	AGPIO3	LPC_CLK0	LPC_CLK1	LPC_FRAME#
Type	I	I	I	II	II	II
PULL HIGH	RTC Coin Battery is implemented DEFAULT	NORMAL POWR UP /RESET TIMING DEFAULT	Enhanced Reset logic DEFAULT	Boot Fail Timer Enabled	CLKGEN ENABLED DEFAULT	SPI ROM DEFAULT
PULL LOW	RTC Coin Battery is not implemented	Reserved	Traditional Reset logic	Boot Fail Timer Disabled DEFAULT	Reserved	LPC ROM

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CPU Strappings

SizeA4

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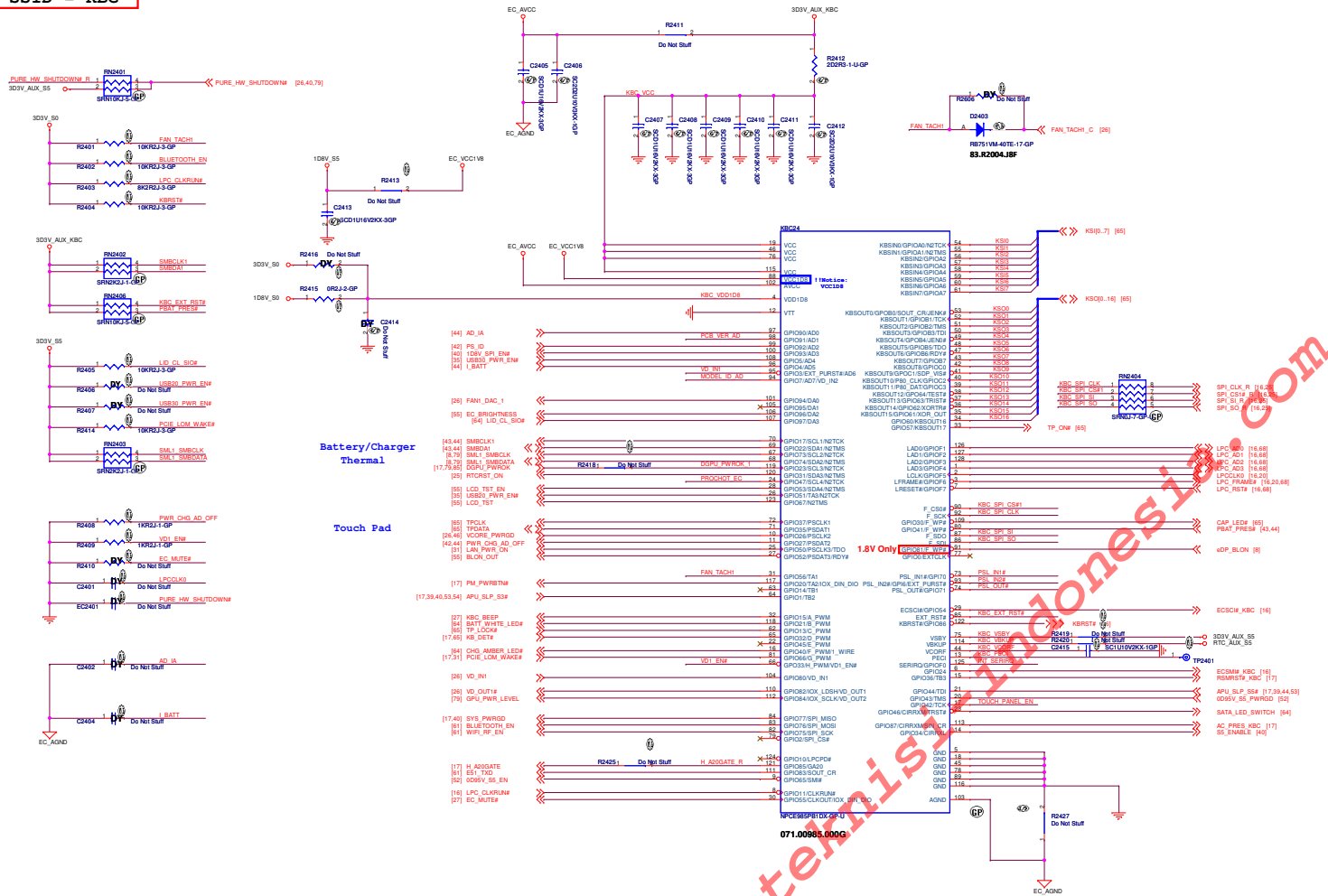
(Blanking)

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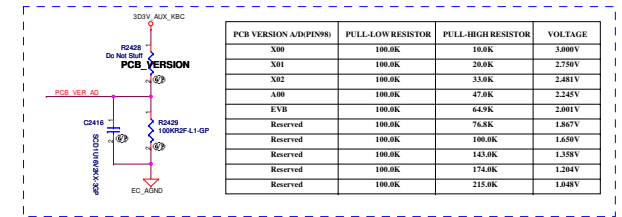
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SSID = KBC



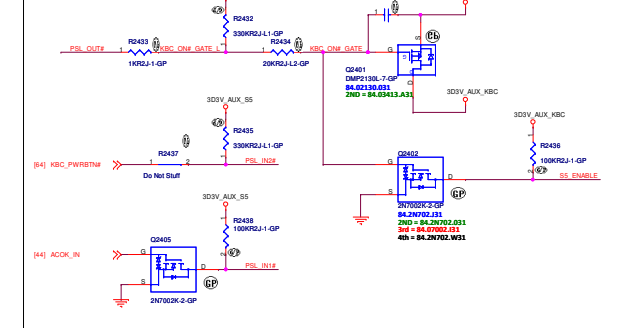
PCB Version



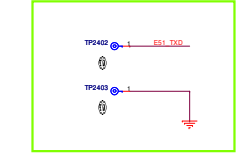
R2453 for SKU Control

MODEL_ID_DET(GPIO7)	PULL-LOW RESISTOR	PULL-HIGH RESISTOR	VOLTAGE
100	100.0K	10.0K	3.000V
101	100.0K	20.0K	2.750V
102	100.0K	33.0K	2.481V
103	100.0K	47.0K	2.245V
104	100.0K	64.0K	2.001V
105	100.0K	75.0K	1.867V
106	100.0K	100.0K	1.650V
107	100.0K	143.0K	1.358V
108	100.0K	174.0K	1.204V
109	100.0K	215.0K	1.045V

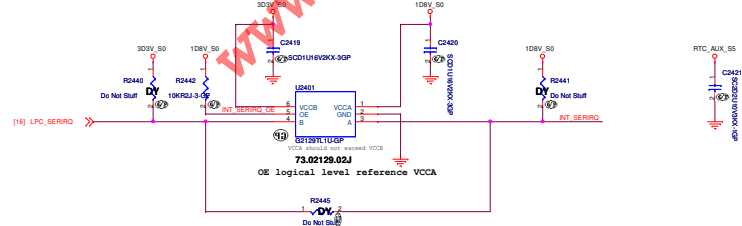
Start up



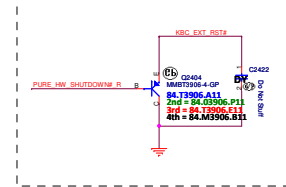
Layout Note: Place it on Bottom Side.



Level shift

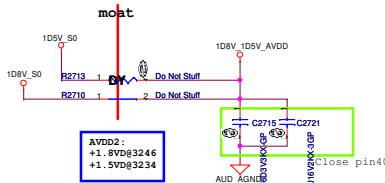
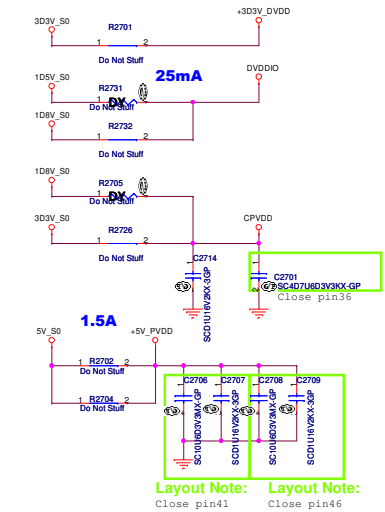


KBC_RESET

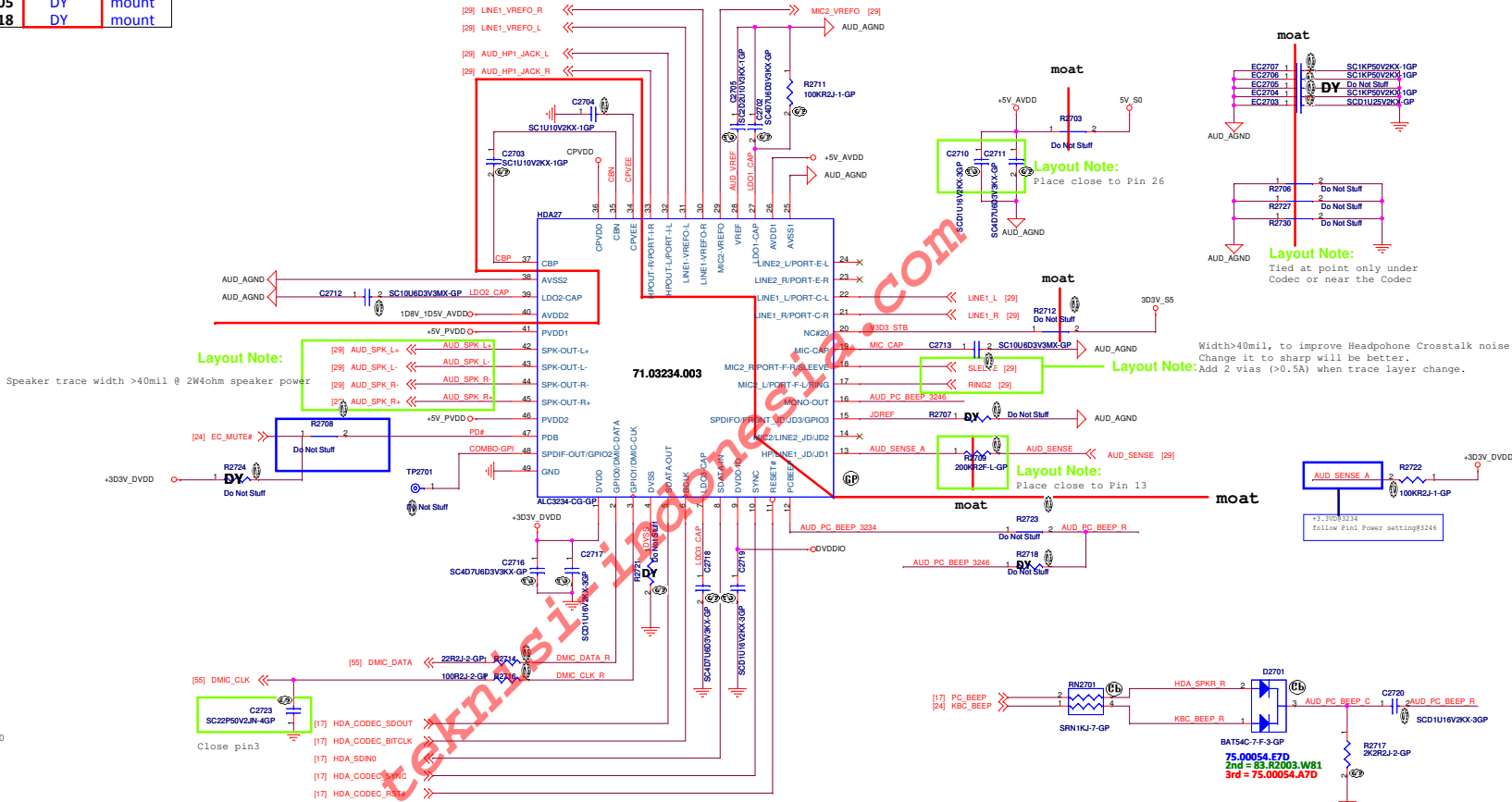
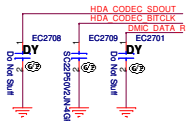


	ALC3234	ALC3246
R2726	mount	DY
R2723	mount	DY
R1740	mount	DY
R2713	DY	DY
R2710	mount	mount
R2705	DY	mount
R2718	DY	mount

Try to Use 1D8V on 3234.



Azalia I/F EMI



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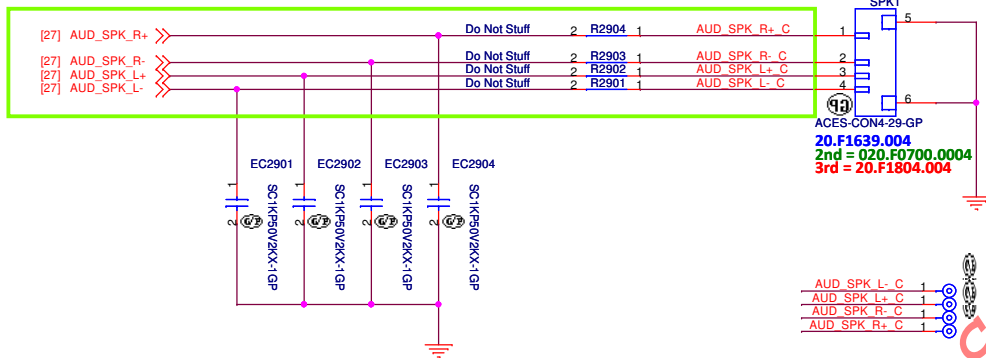
Date: Monday, August 29, 2016

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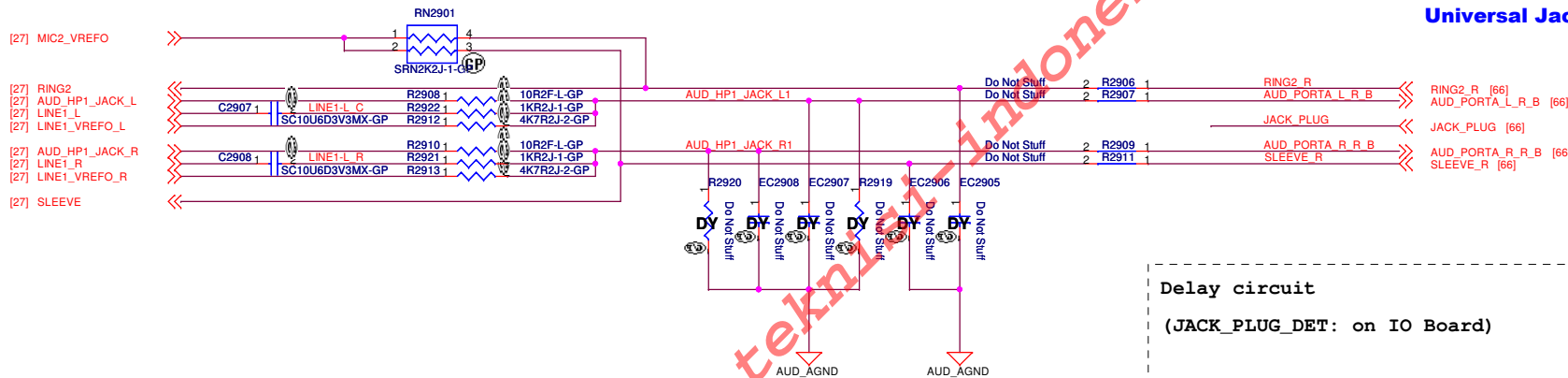
SSID = Speaker/Universal Jack

Layout Note:

Speaker trace width >40mil @ 2W4ohm speaker power



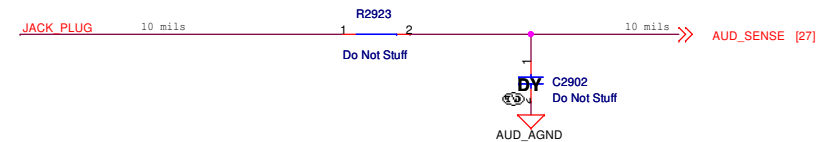
CONN Pin	Net name
Pin1	SPK_R+
Pin2	SPK_R-
Pin3	SPK_L+
Pin4	SPK_L-



Universal Jack (Moved to I/O Board)

Delay circuit

(JACK_PLUG_DET: on IO Board)

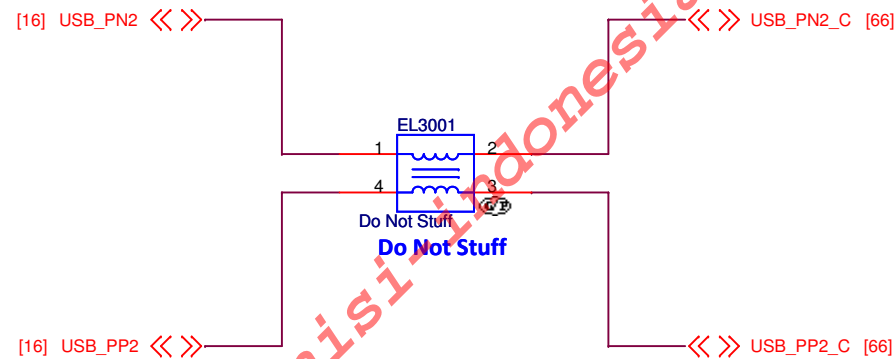


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Title Speaker/Universal Jack		
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Main Func = Card Reader



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Title

Card Reader

Size
A4

Document Number

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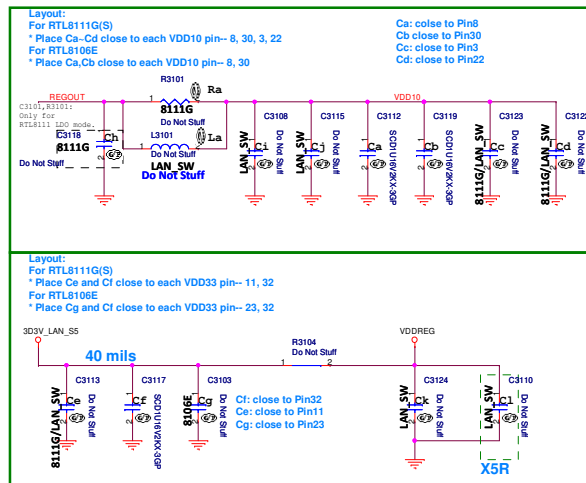
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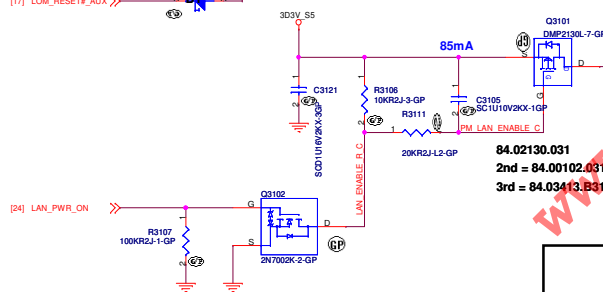
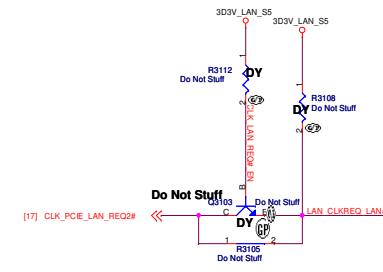
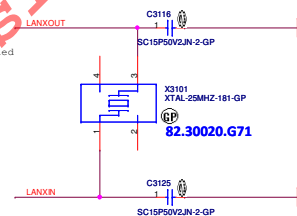
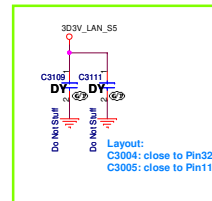
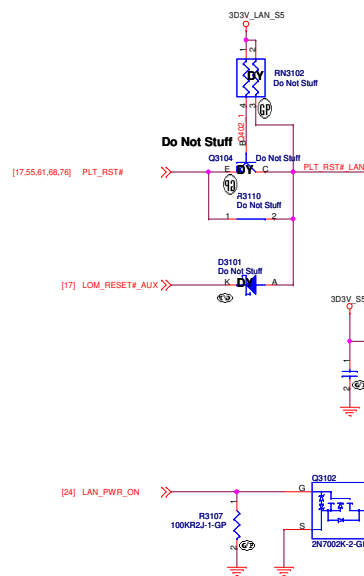
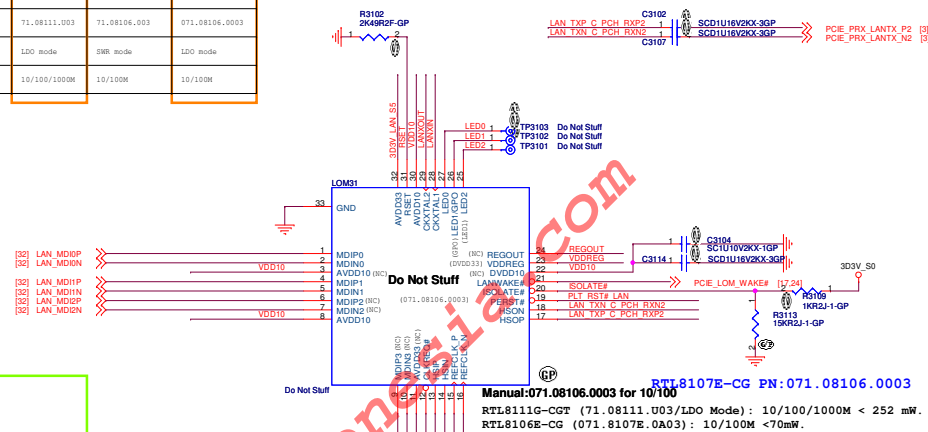
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SSID = LAN RTL8111/RTL8106



LAN CHIP (10/100/1000M & 10/100M co-lay)


RTL8111GUS-CG	RTL8111G-CGT	RTL8106EUS-CG	RTL8106E-CG
71.08111.W03	71.08111.W03	71.08106.W03	071.08106.0003
SWR mode	LDO mode	SWR mode	LDO mode
10/100/1000M	10/100/1000M	10/100M	10/100M

[illegible]

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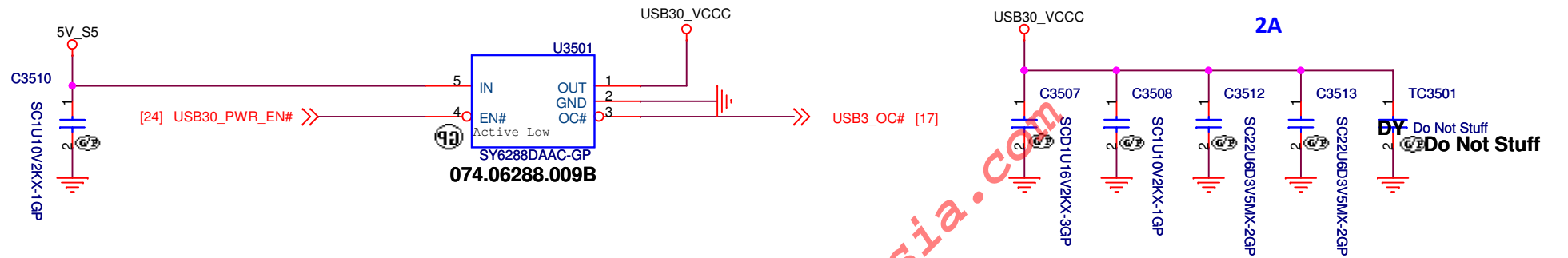
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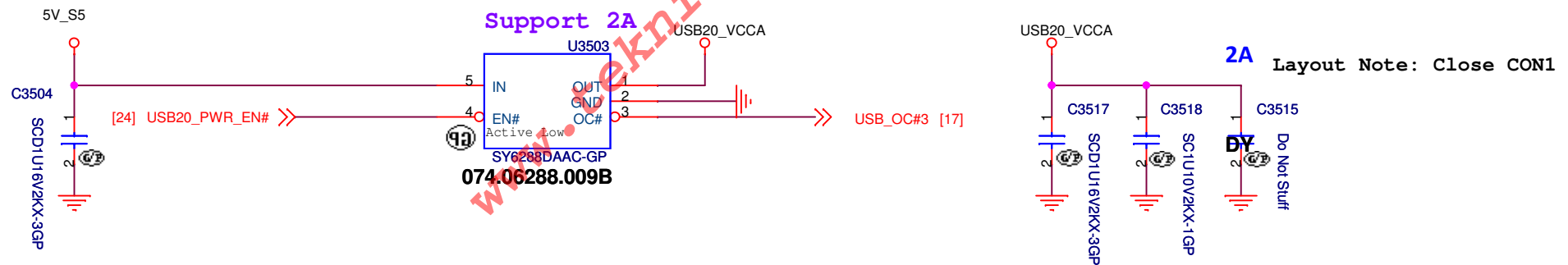
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Main Func = USB3.0



Main Func = USB2.0



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Title

USB Power SW

Size

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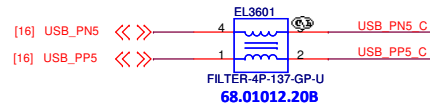
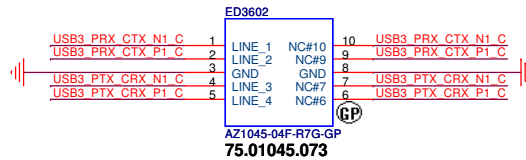
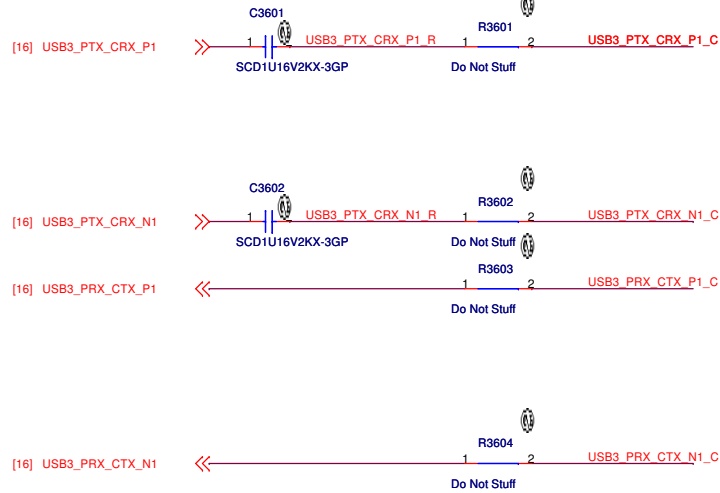
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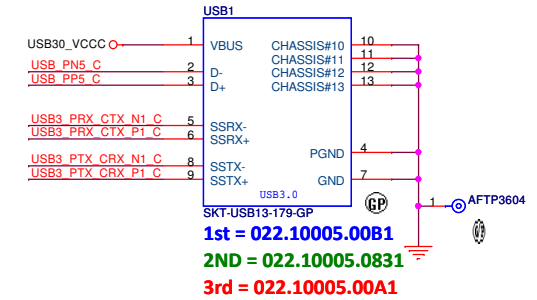
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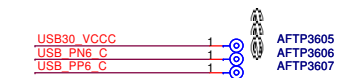
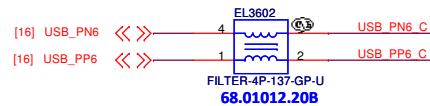
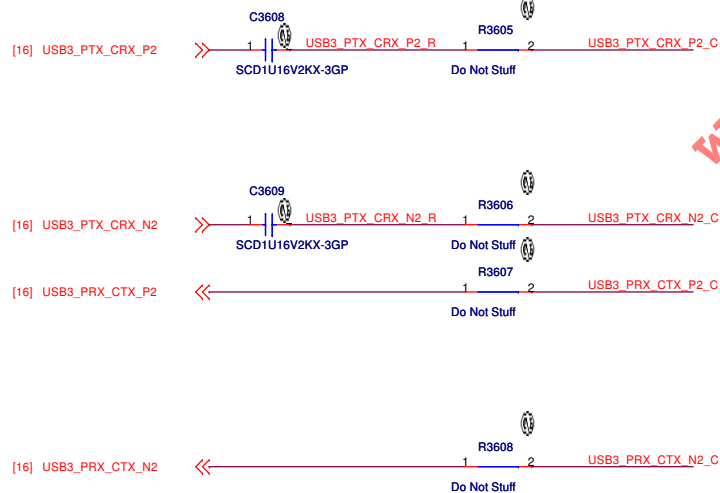
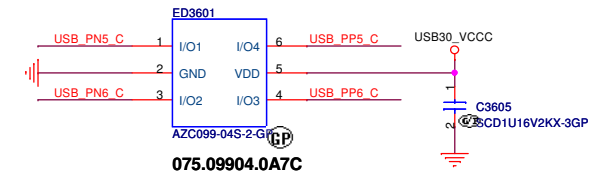
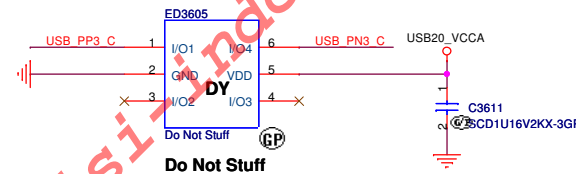
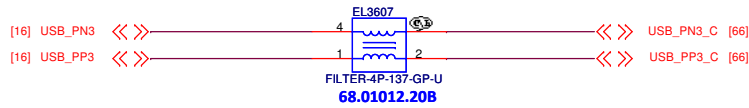
Main Func = USB3.0 Port1/2



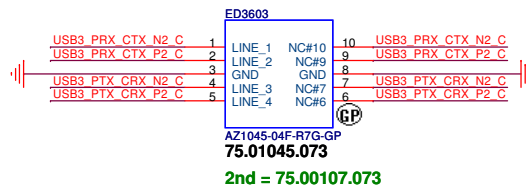
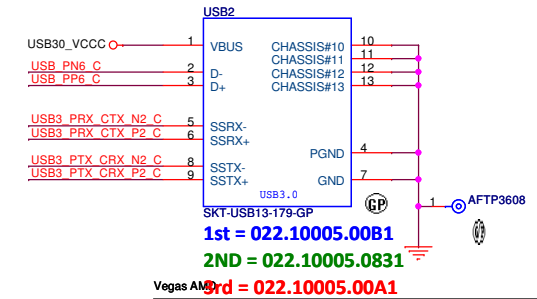
USB3.0 Port1



USB2.0 Port3




USB3.0 Port2



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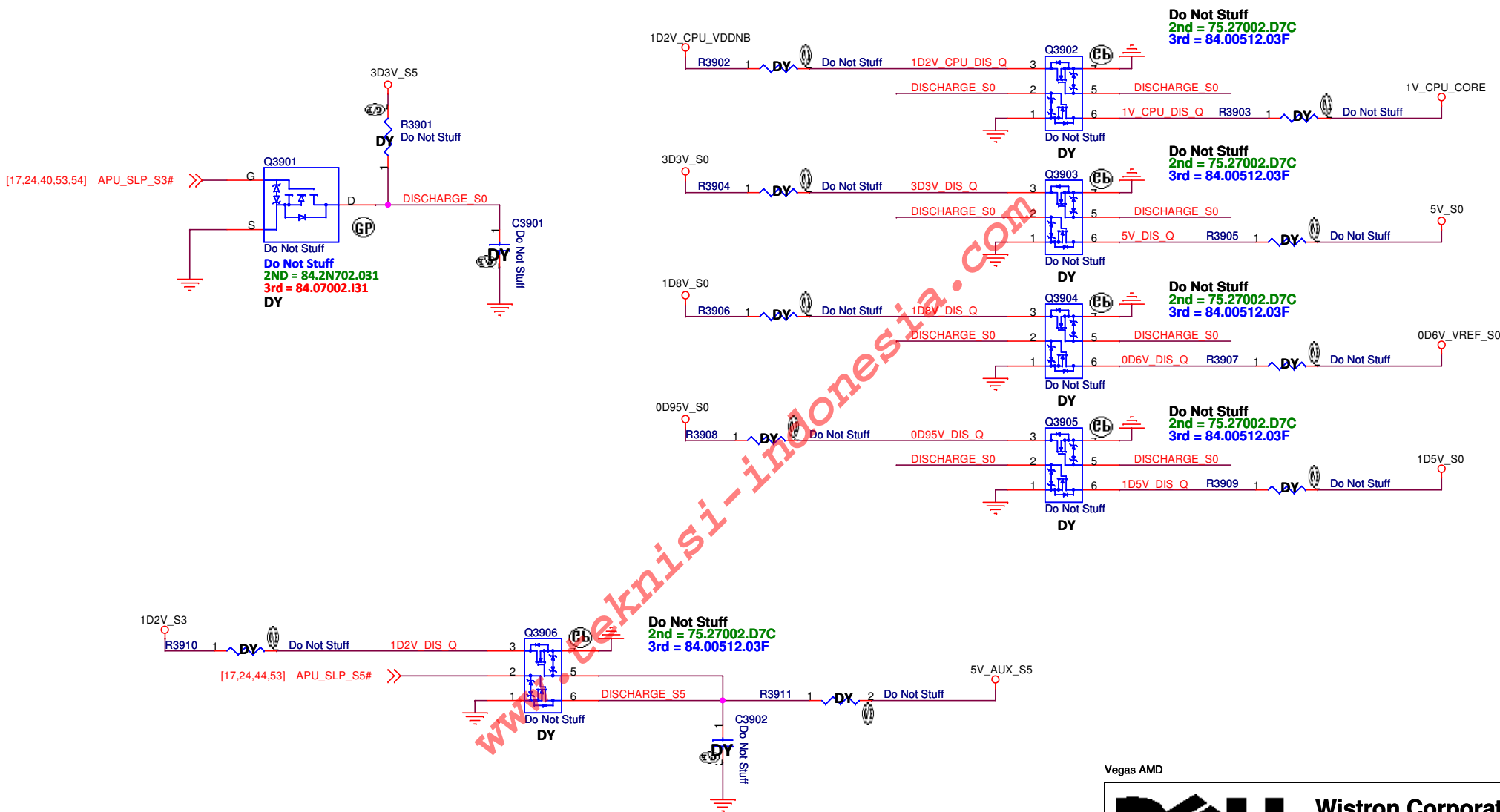
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Discharge Circuit



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DISCHARGE		
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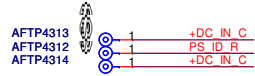
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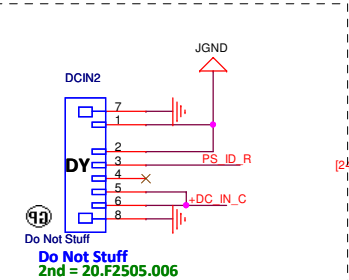
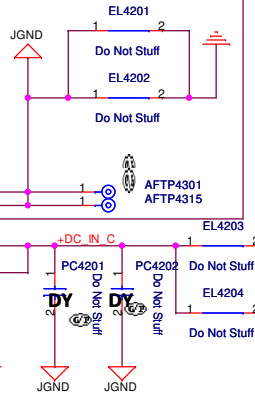
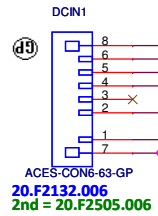
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SSID = DCIN



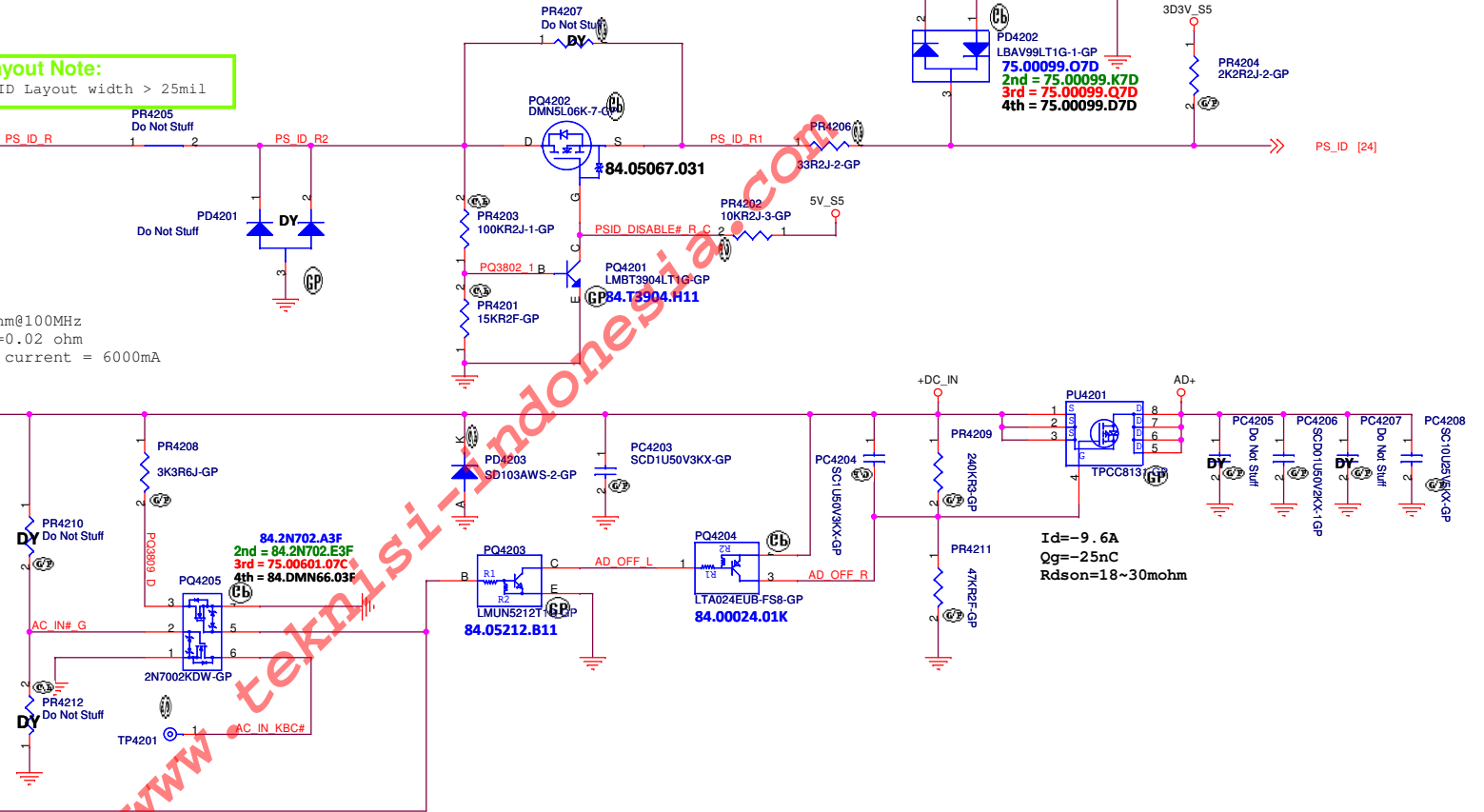
Layout Note:
PSID Layout width > 25mil

60ohm@100MHz
DCR=0.02 ohm
Max current = 6000mA



for NOVA add 1/22

[24,44] PWR_CHG_AD_OFF



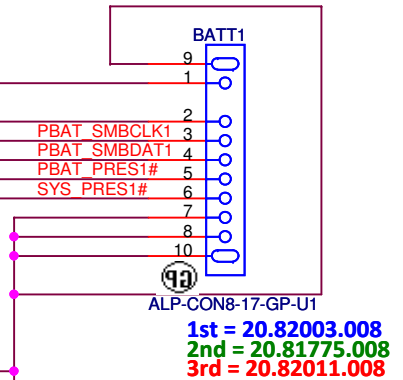
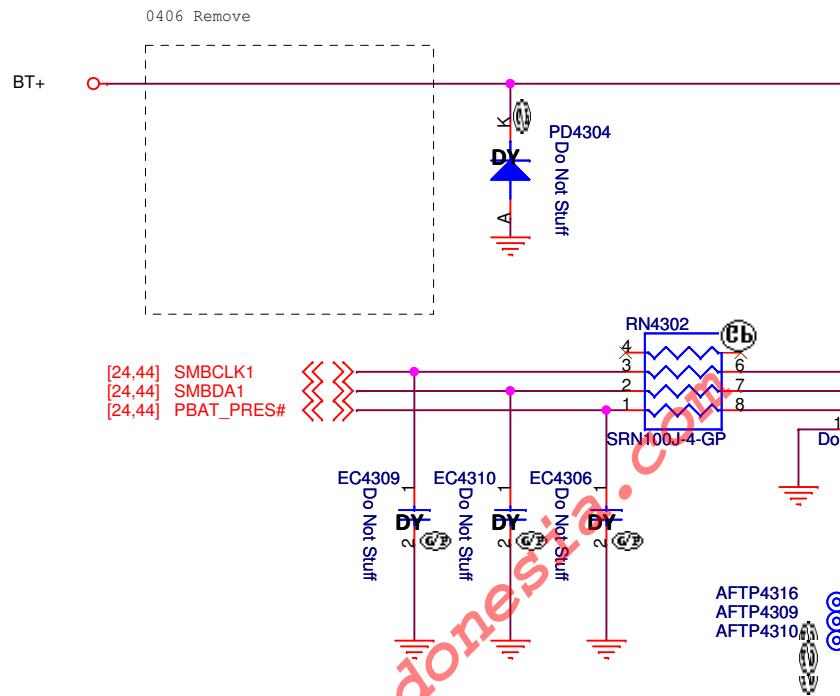
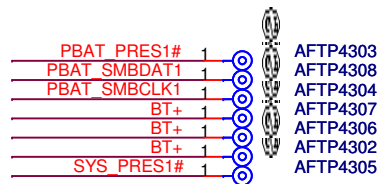
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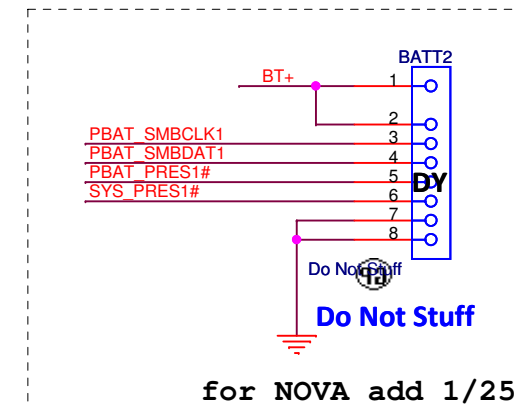
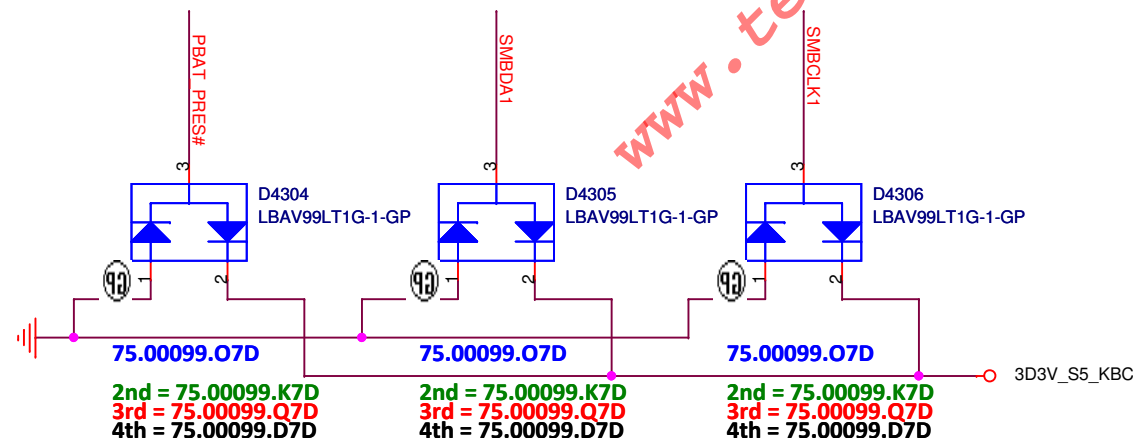
Title **DCIN JACK**
Size A3 Document Number **Turis STR (FT4)** Rev **A00**
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SSID = BATT CONN

Batt Connector



Placement: Close to Batt Connector



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Title

BATT CONN

Size
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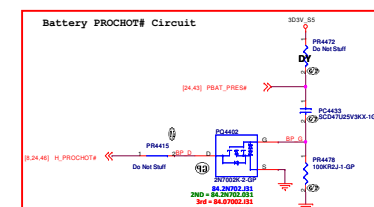
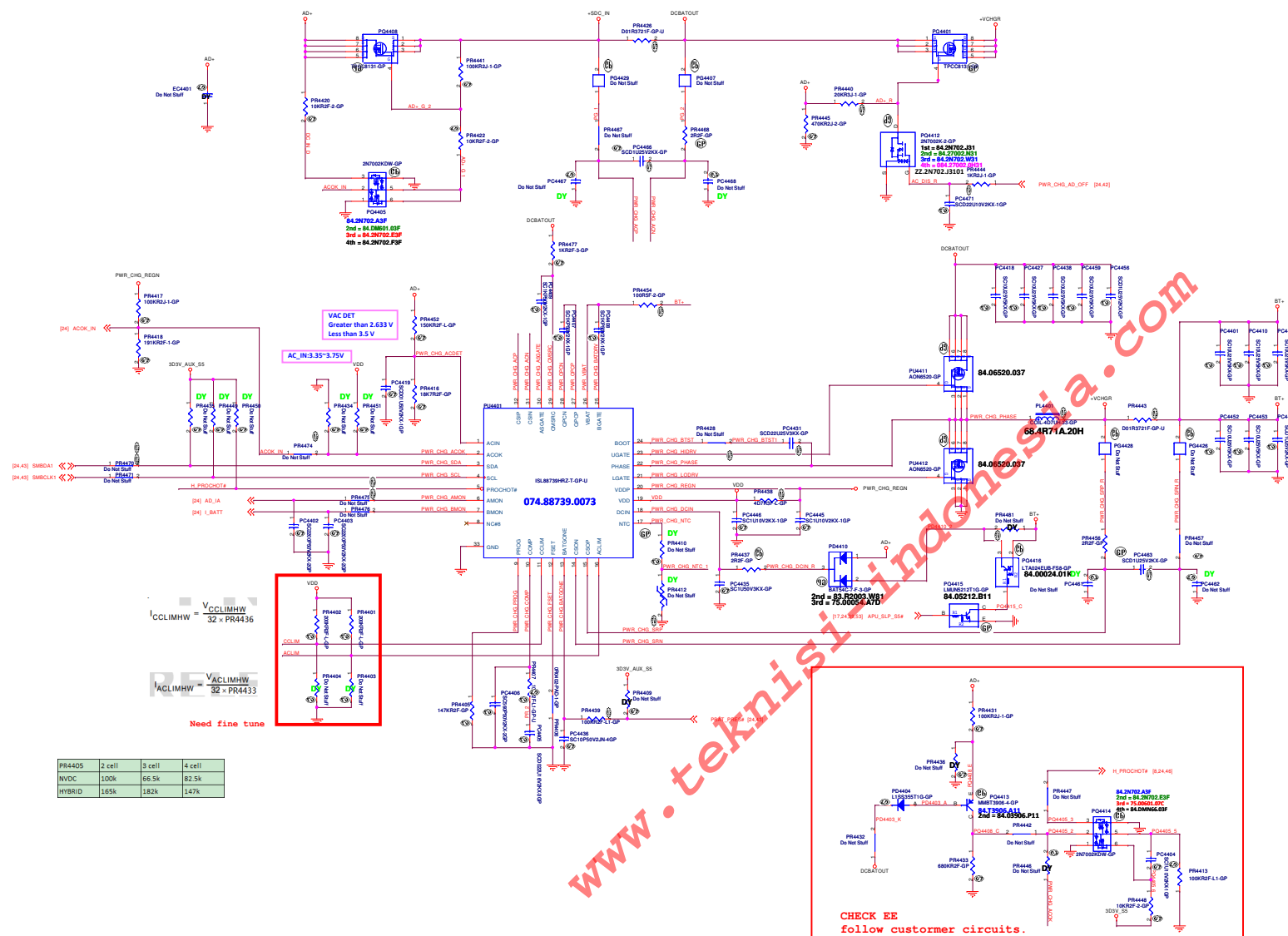
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SSID = PWR.Plane.Regulator_5v3p3v

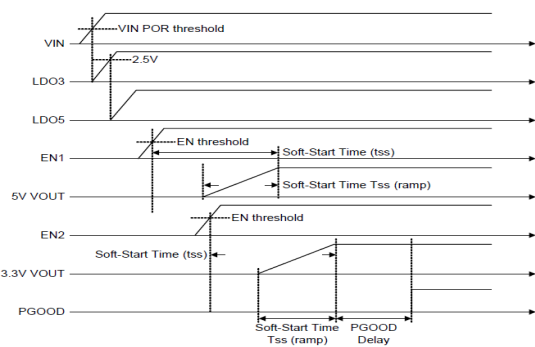
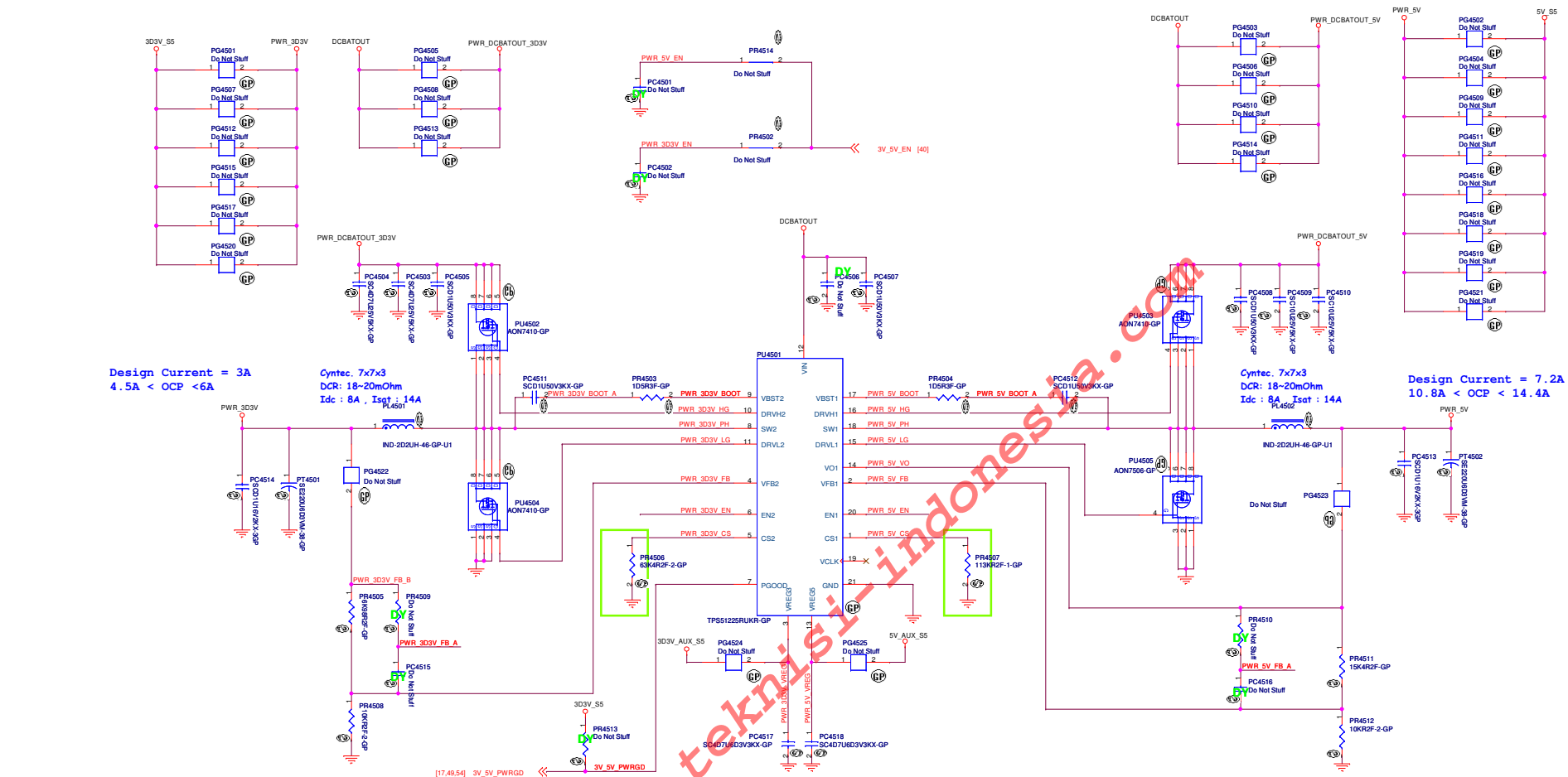
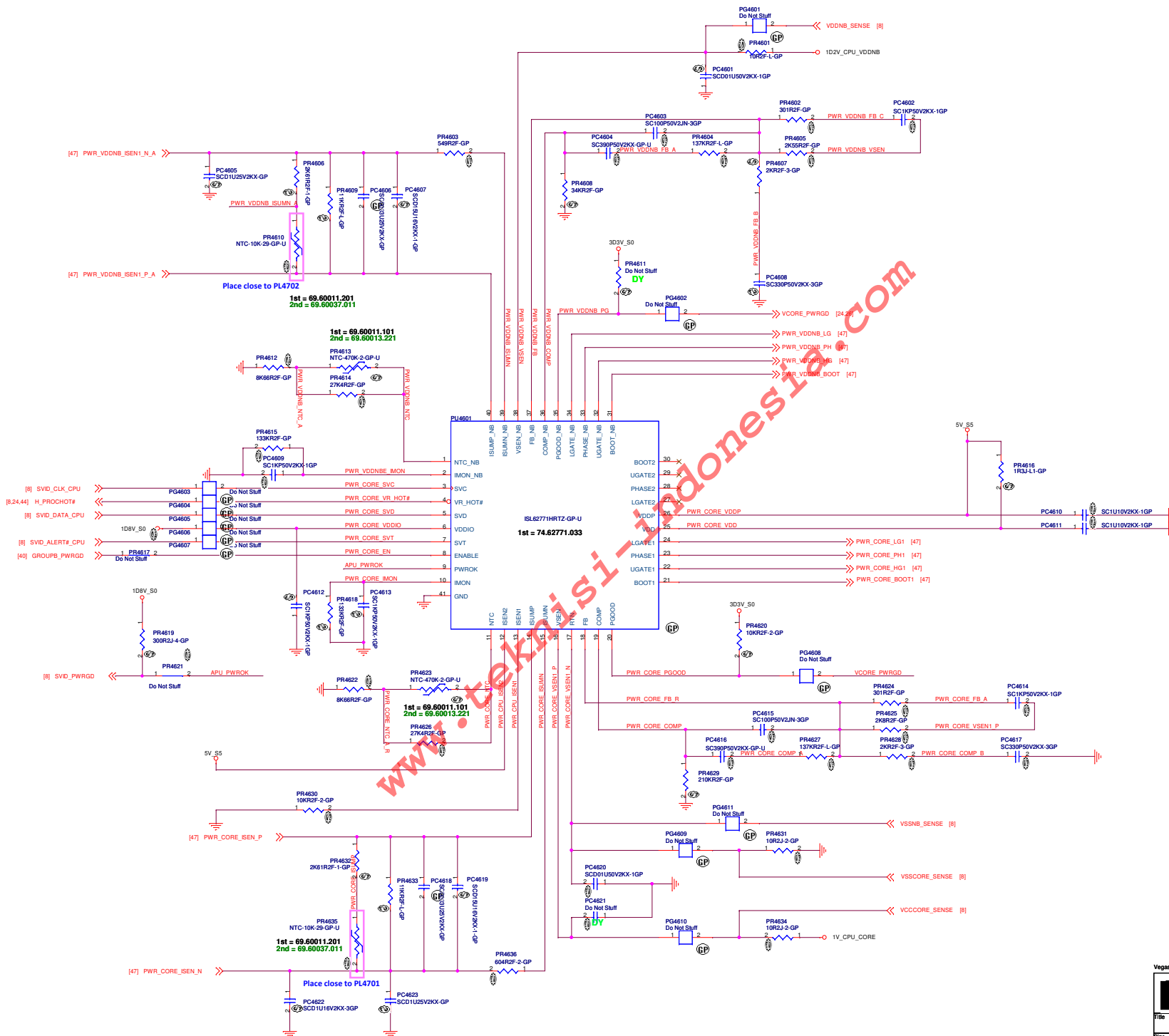


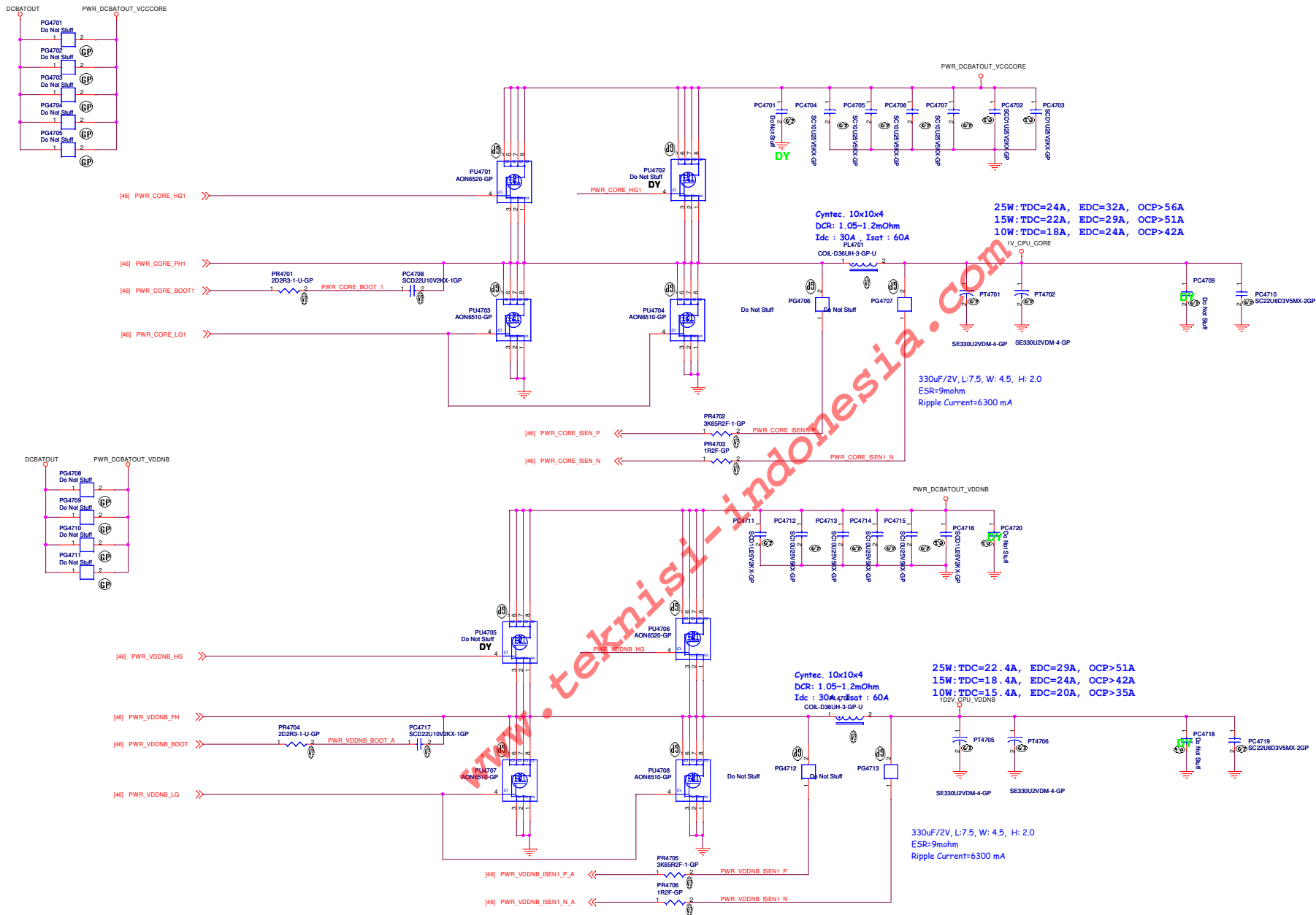
Figure 6. RT6575B Timing

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```
SSID = CPU.Regulator
```



SSID = CPU.Regulator



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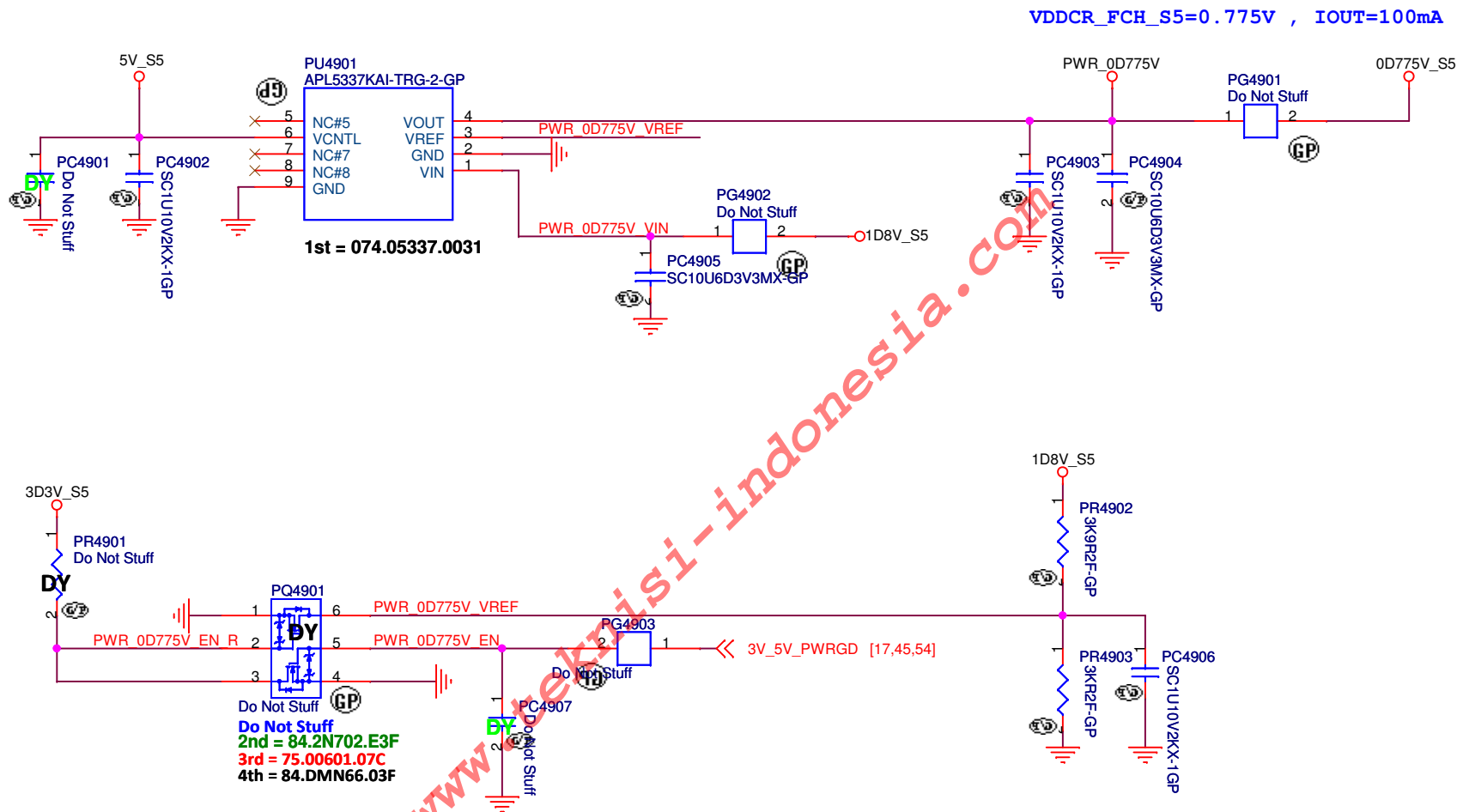
(Blanking)

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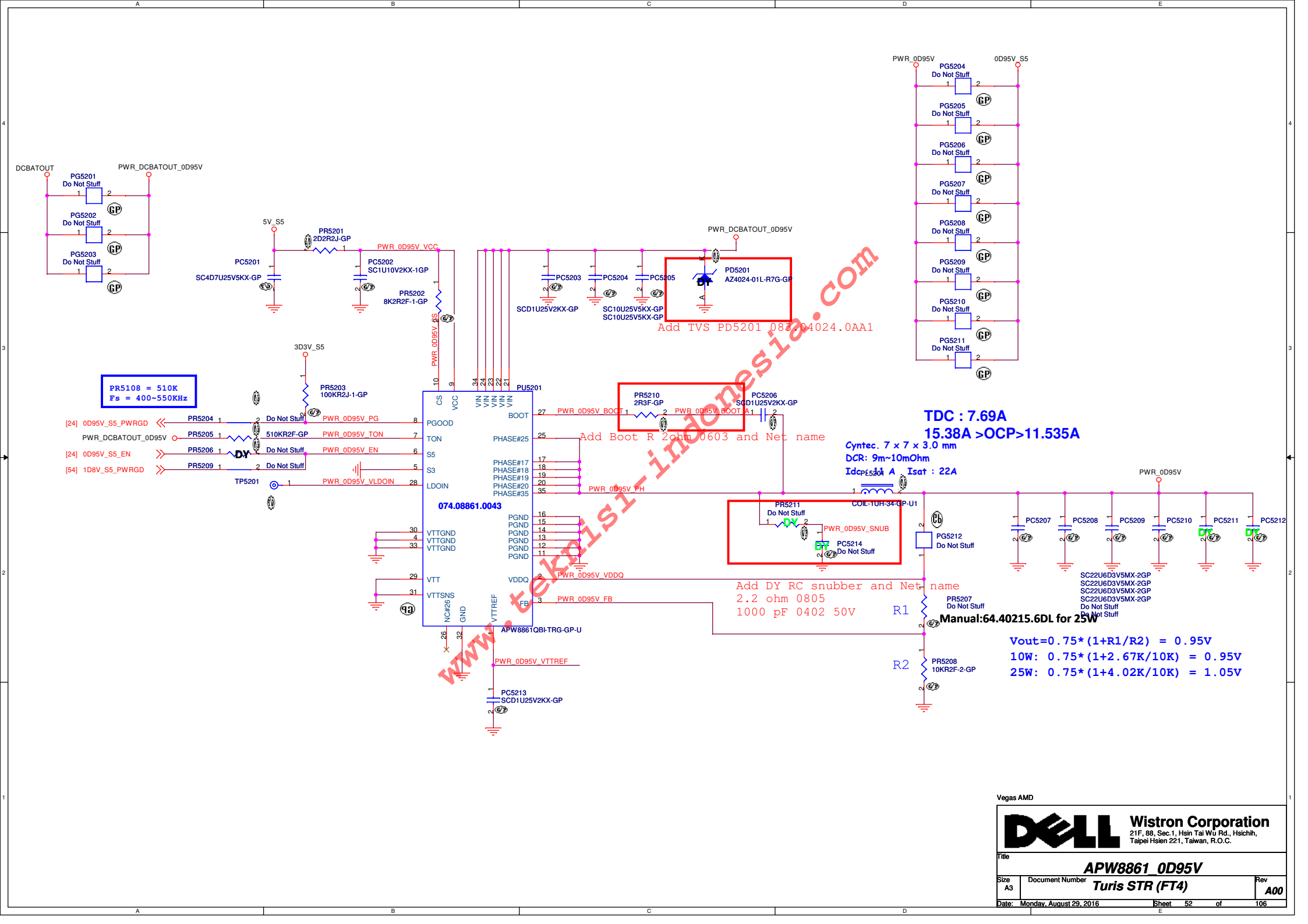
		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
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PR5108 = 510K
Fs = 400~550KHz

Add TVS PD5201 083 04024.0AA1

Add Boot R 2ohm 0603 and Net name

Add DY RC snubber and Net name
2.2 ohm 0805
1000 pF 0402 50V

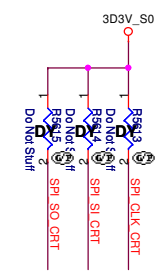
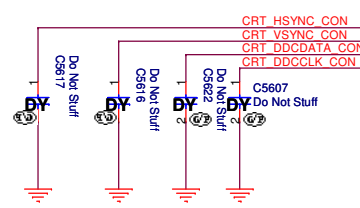
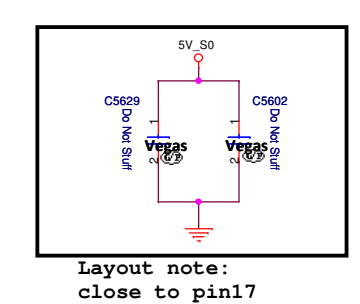
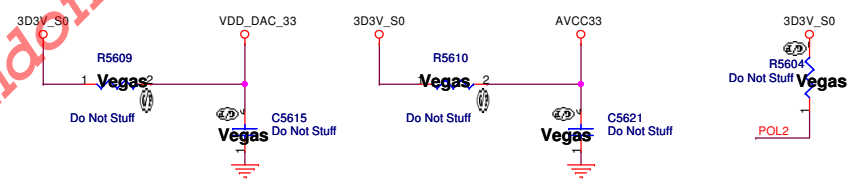
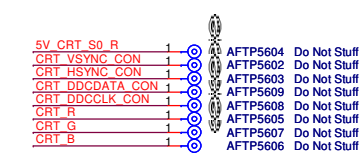
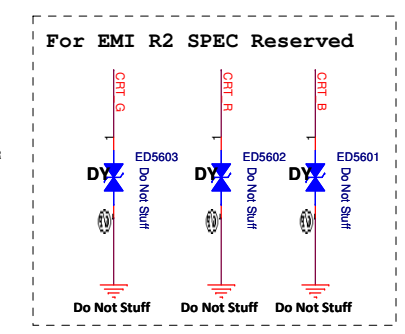
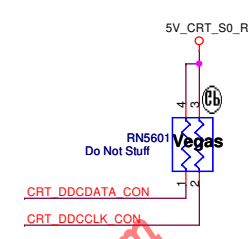
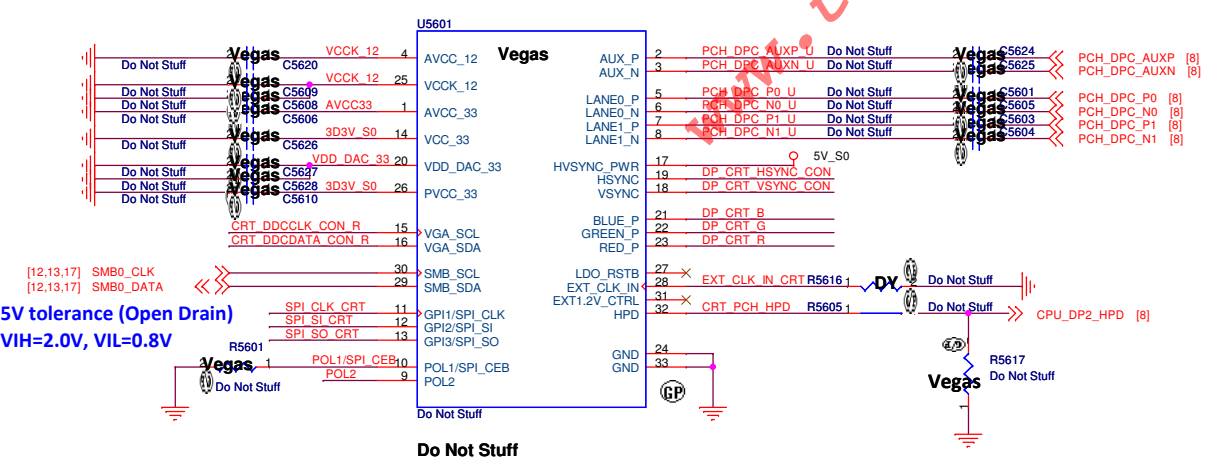
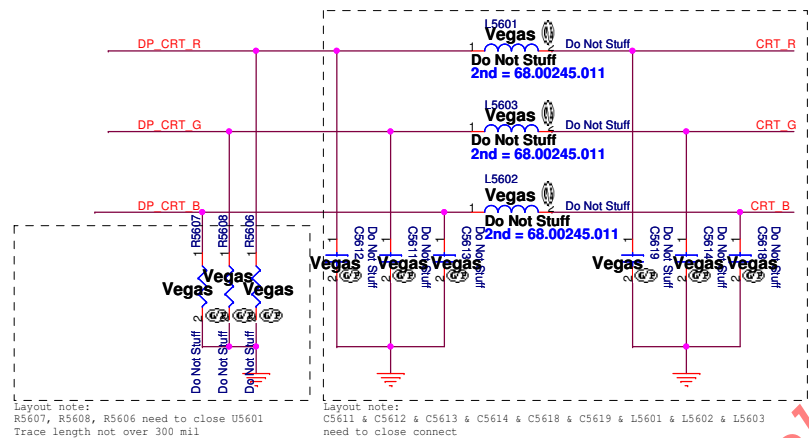
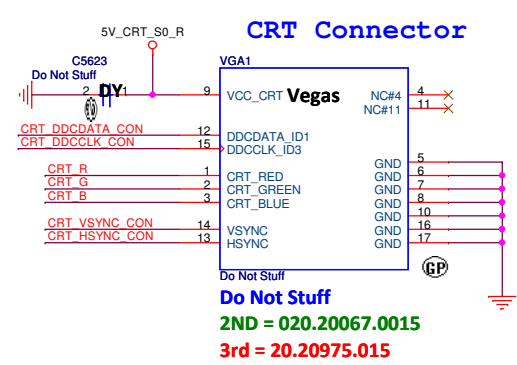
TDC : 7.69A
15.38A > OCP > 11.535A

Cyntec. 7 x 7 x 3.0 mm
DCR: 9m~10mOhm
Idc: 5.4A Isat : 22A

Manual:64.40215.6DL for 25W

$$V_{out} = 0.75 * (1 + R1/R2) = 0.95V$$
$$10W: 0.75 * (1 + 2.67K/10K) = 0.95V$$
$$25W: 0.75 * (1 + 4.02K/10K) = 1.05V$$

SSID = DP to VGA Converter



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Title

VGA

Size
A3

Document Number	
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ment Number

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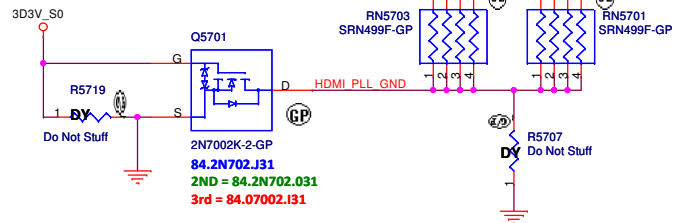
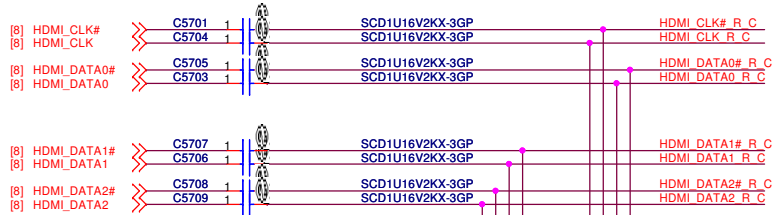
August 29, 2016 Sheet

Rev	A00
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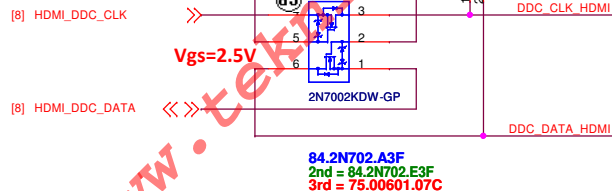
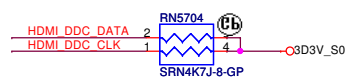
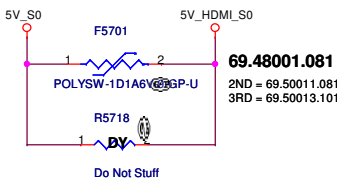
A3	Turis
Date: Monday, August 29, 2016	

(FT4)	A00
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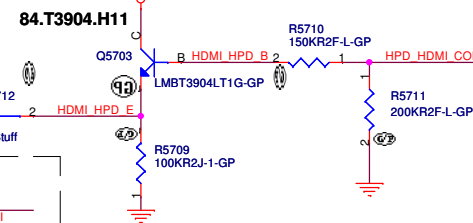
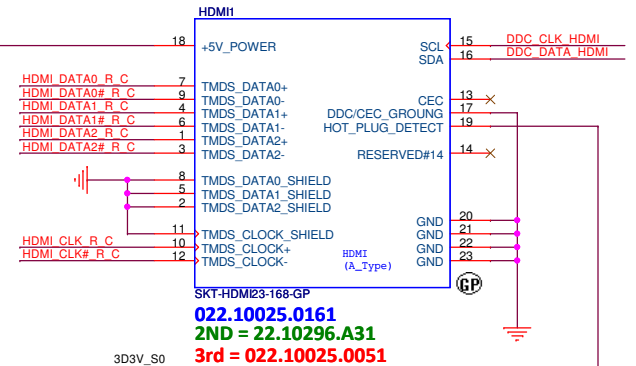
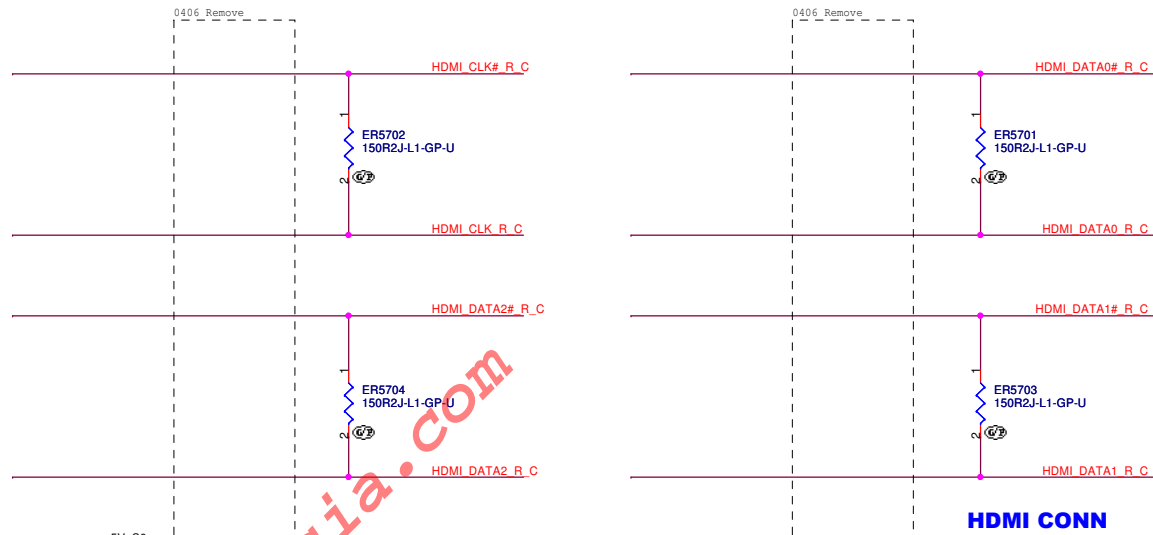
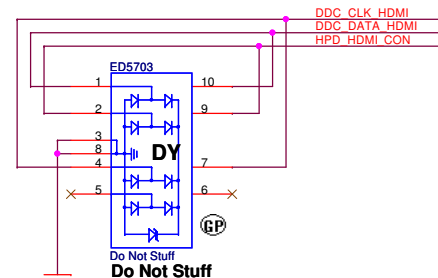
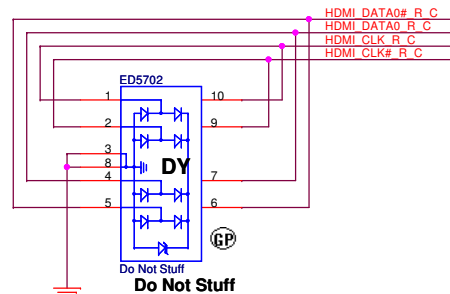
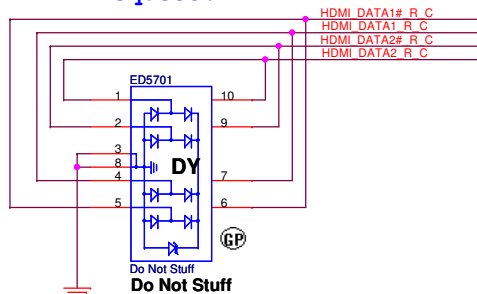
SSID = HDMI Level Shifter/Connector



69.50007.691:
OBS REASON: Please transfer to down size item 69.48001.081 for cost reduction and good cost down trend



EMI Request:



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Title		
(Reserved)		
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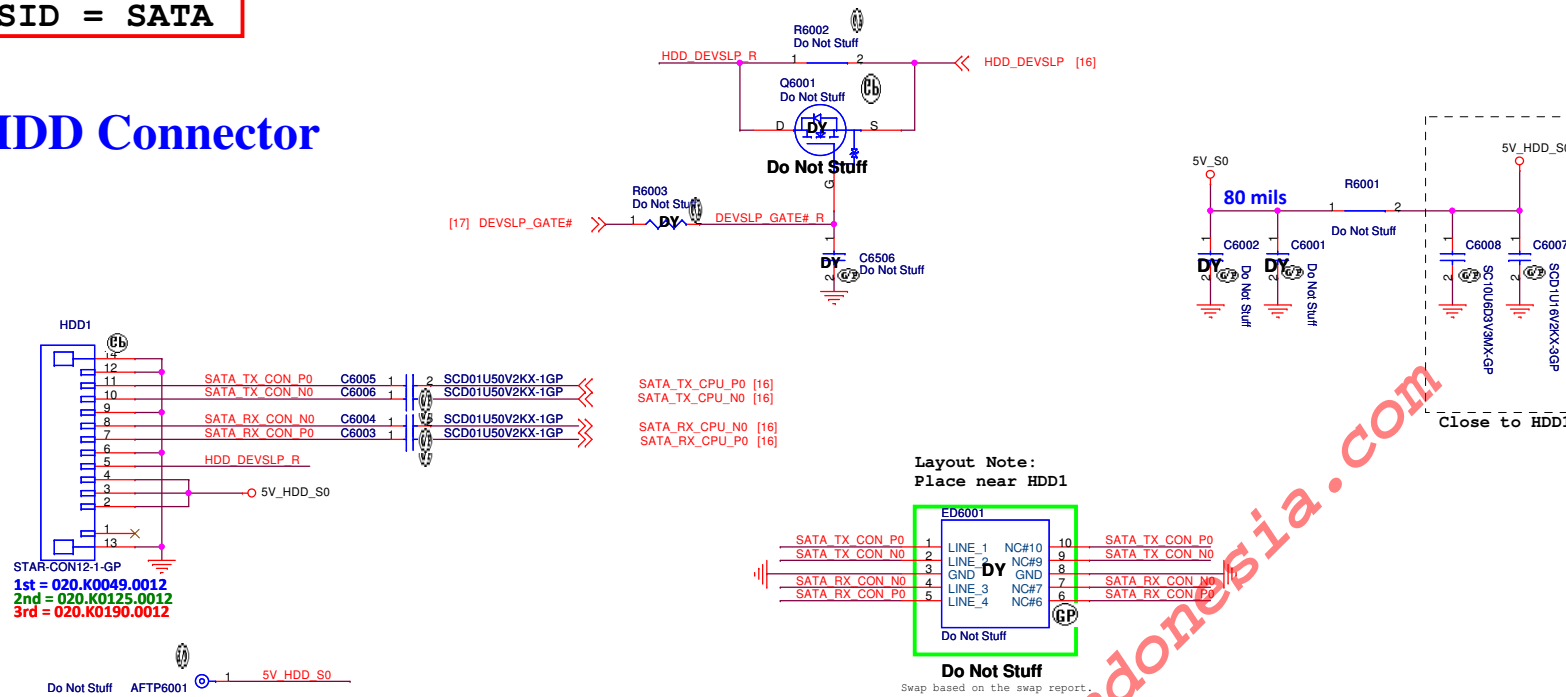
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Title					
(Reserved)					
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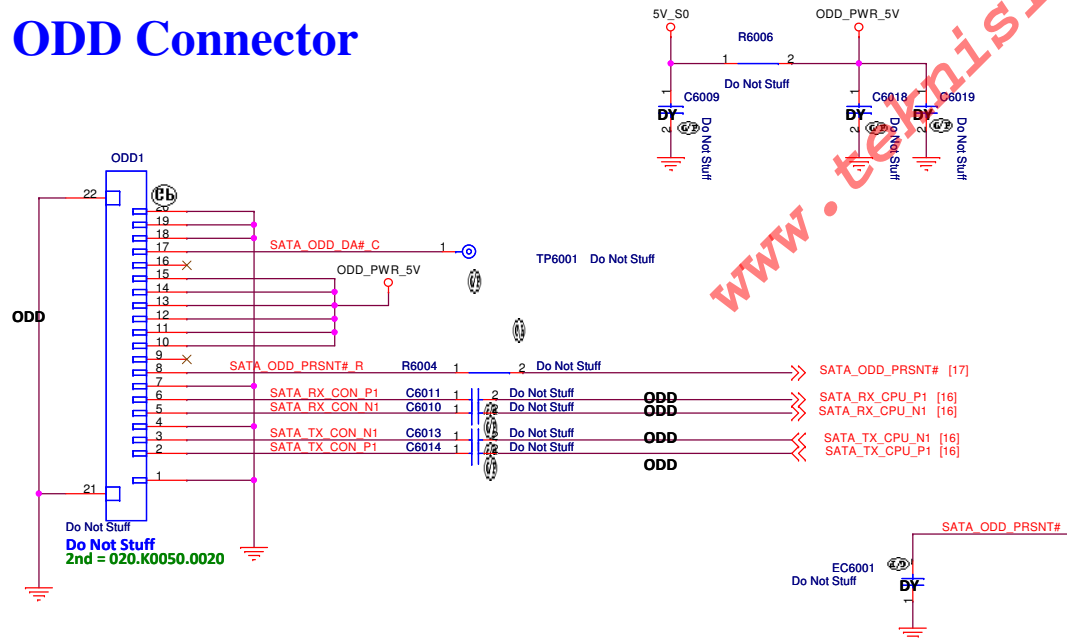
SSID = SATA

HDD Connector

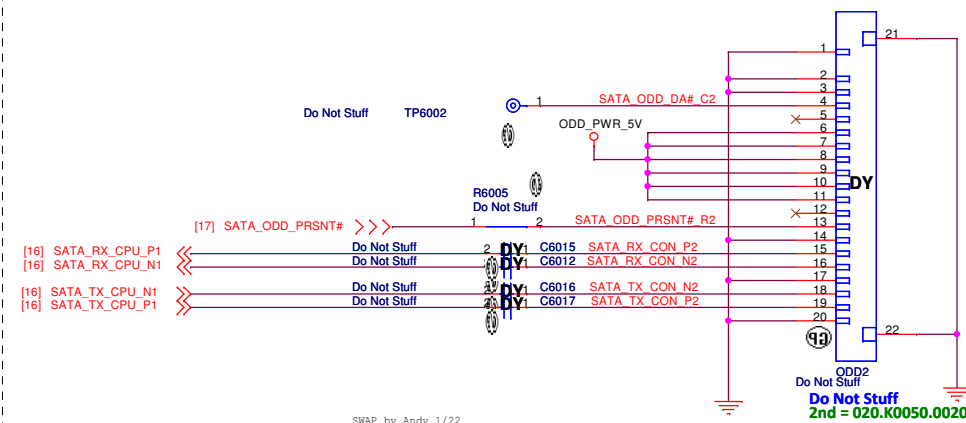


CONN		FFC
GND	S1	1
A+	S2	2
A-	S3	3
GND	S4	4
B-	S5	5
B+	S6	6
GND	S7	7
GND	P1	
GND	P2	
GND	P3	
5V	P4	10
5V	P5	11
5V	P6	12
GND	P7	
GND	P8	

ODD Connector



For NOVA if use ODD2, the cable need to add ont type



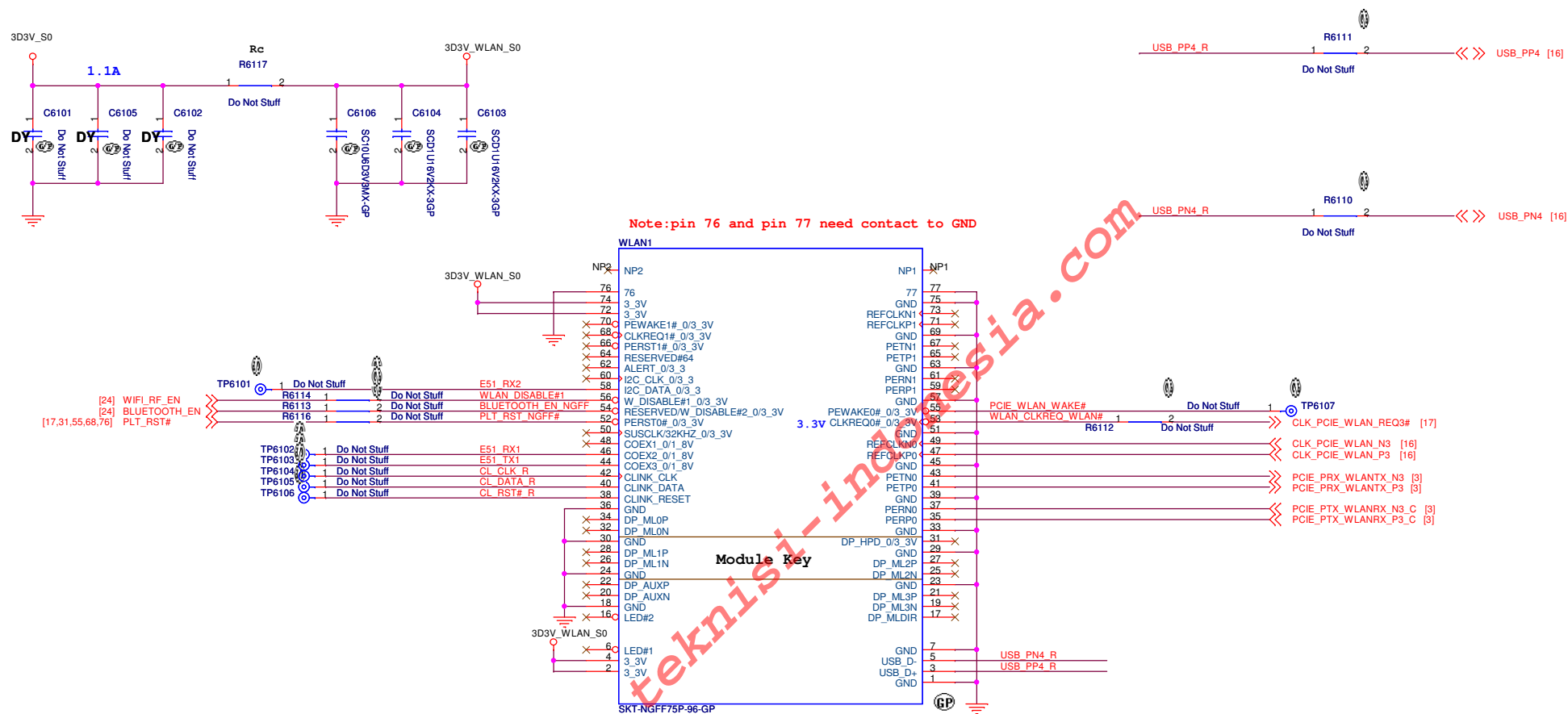
Vegas AMD



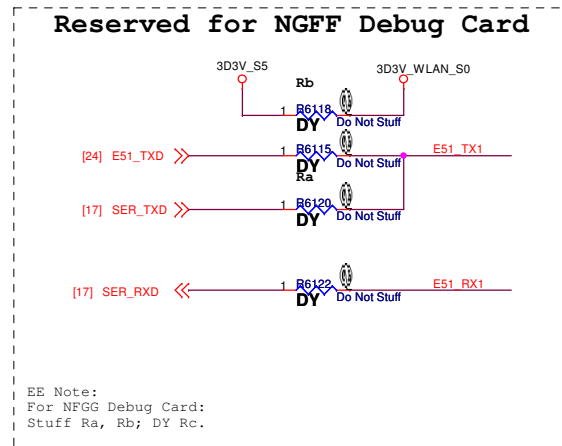
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Title SATA IF HDD/ODD		
Size A3	Document Number Turis STR (FT4)	Rev A00
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Main Func = WLAN




1st = 062.10003.0621
2ND = 062.10007.0081
3rd = 062.10007.0391
4th = 62.10043.S31



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Reserved			
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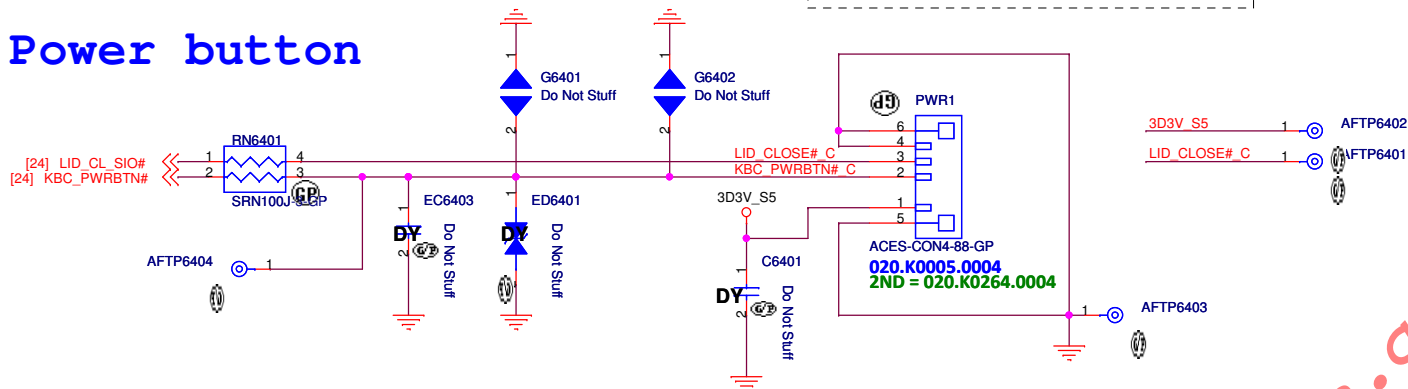
		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title (Reserved)			
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SSID = LED board/Power Button

For EMI Reserved

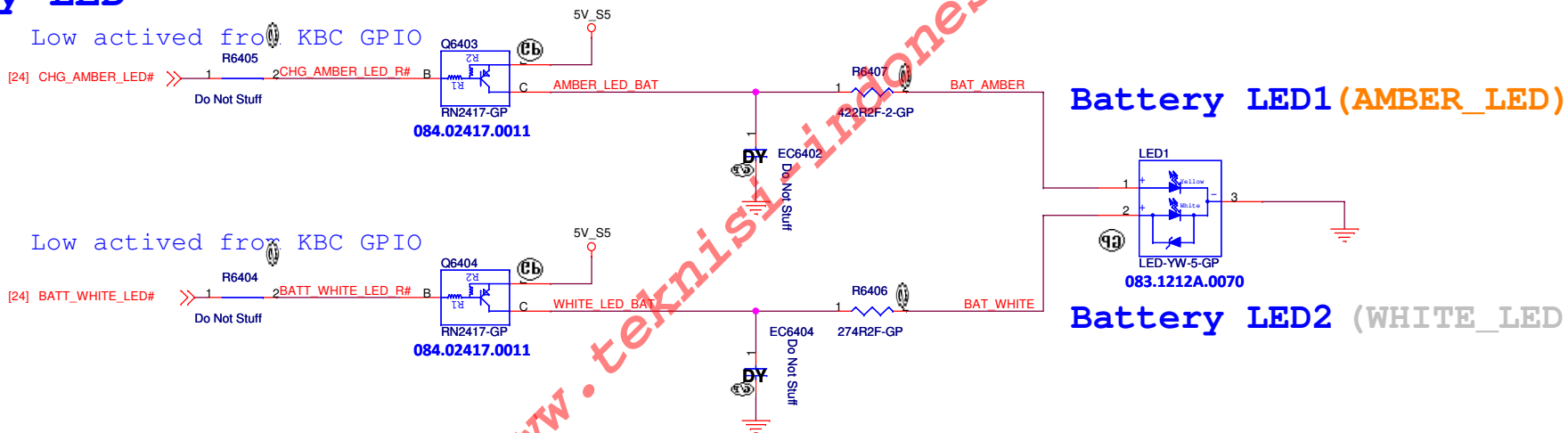
LID_CLOSE# C EC6401 DY 2 Do Not Stuff

Power button



Battery LED

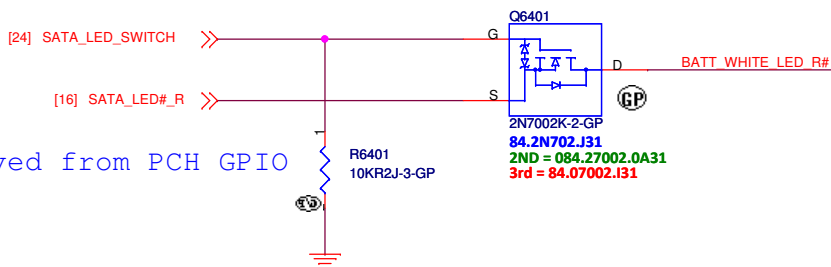
Low active from KBC GPIO



HDD LED

High active from KBC GPIO

Low active from PCH GPIO



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Title

LED Board&Power Button

Size

Document Number

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Rev

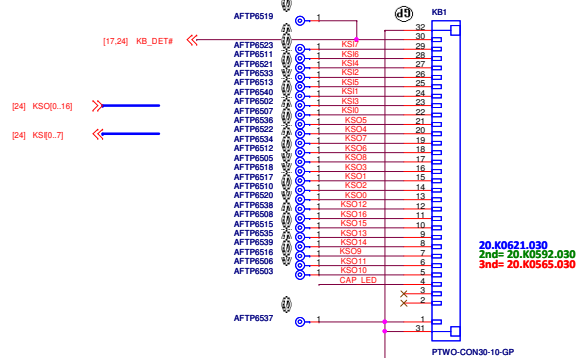
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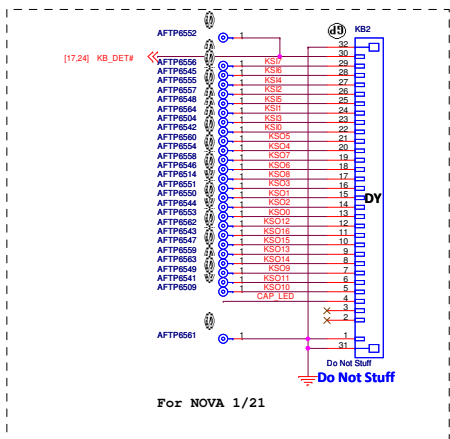
SSID = KBC

Internal Keyboard Connector



CAP LED Control

LOW active from KBC GPIO



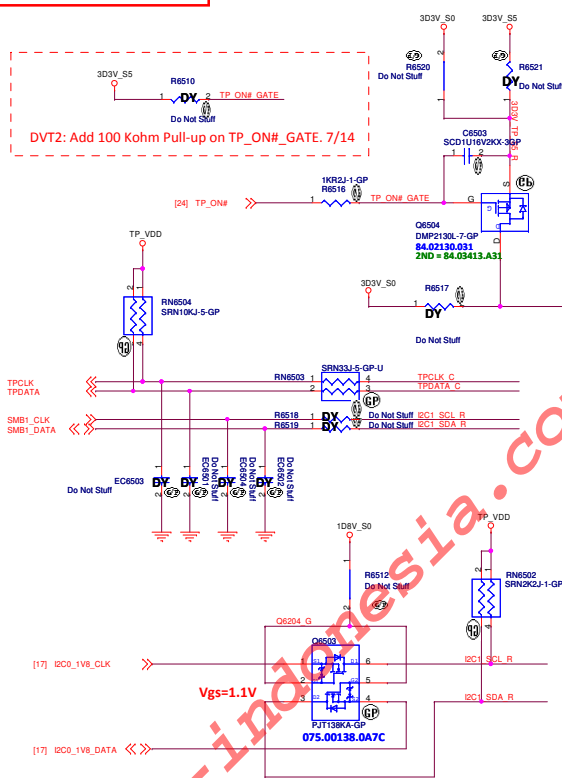
SSID = Touch.Pad

Support PTP

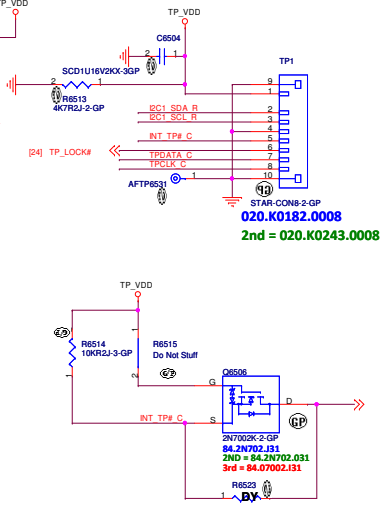
PS2

SMBUS

I2C



Touch Pad Connector



Pin number	Pin name
1	VDD
2	DAT (I2C)
3	CLK (I2C)
4	GND
5	ATTN
6	GPIO
7	DAT (PS2)
8	CLK (PS2)

Pin number	Pin name
1	TP_VDD
2	TPCLK_C
3	TPDATA_C
4	EC1_SCL_R
5	EC1_SDA_R
6	INT_TP#_C
7	TP_LOCK#
8	AFTP6520

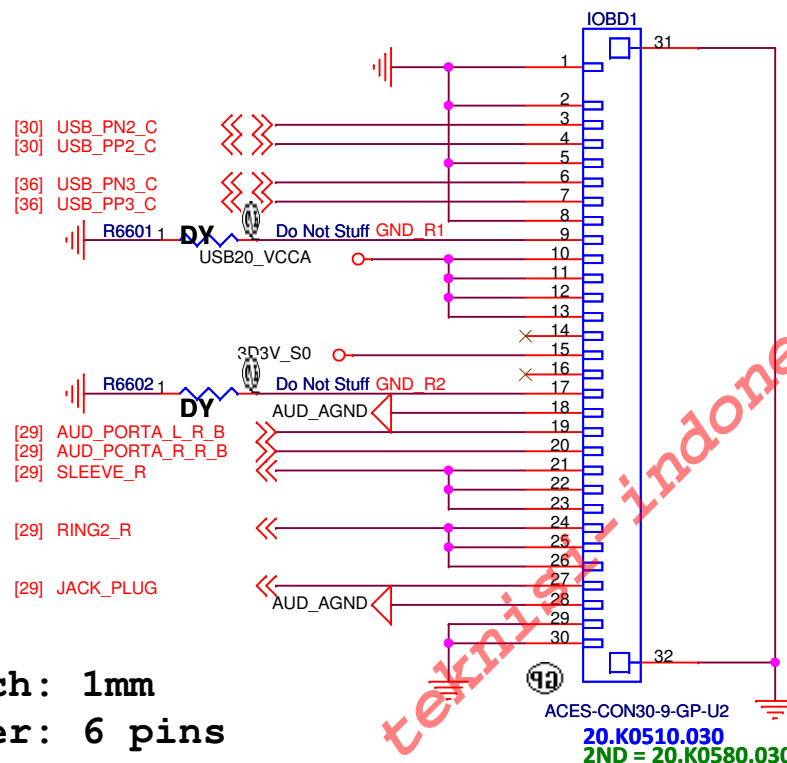
Main Func = IO Connector

I/O Board Connector

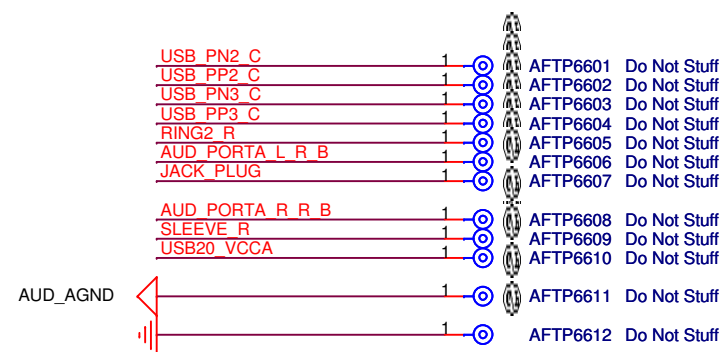
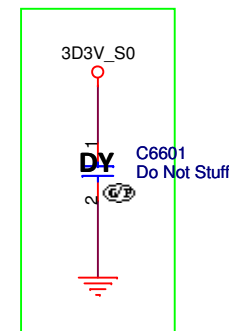
Cardreader
USB3(USB2.0)

Universal Jack

Pitch: 1mm
Power: 6 pins
GND: 7 pins
AGND: 2 Pins



2/2 Add



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Title

IO Board Connector

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Title

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Document Number

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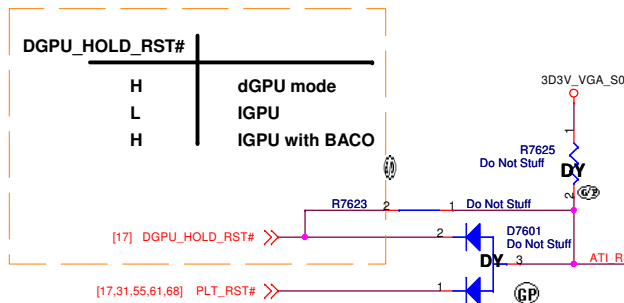
Rev
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Date: Monday, August 29, 2016

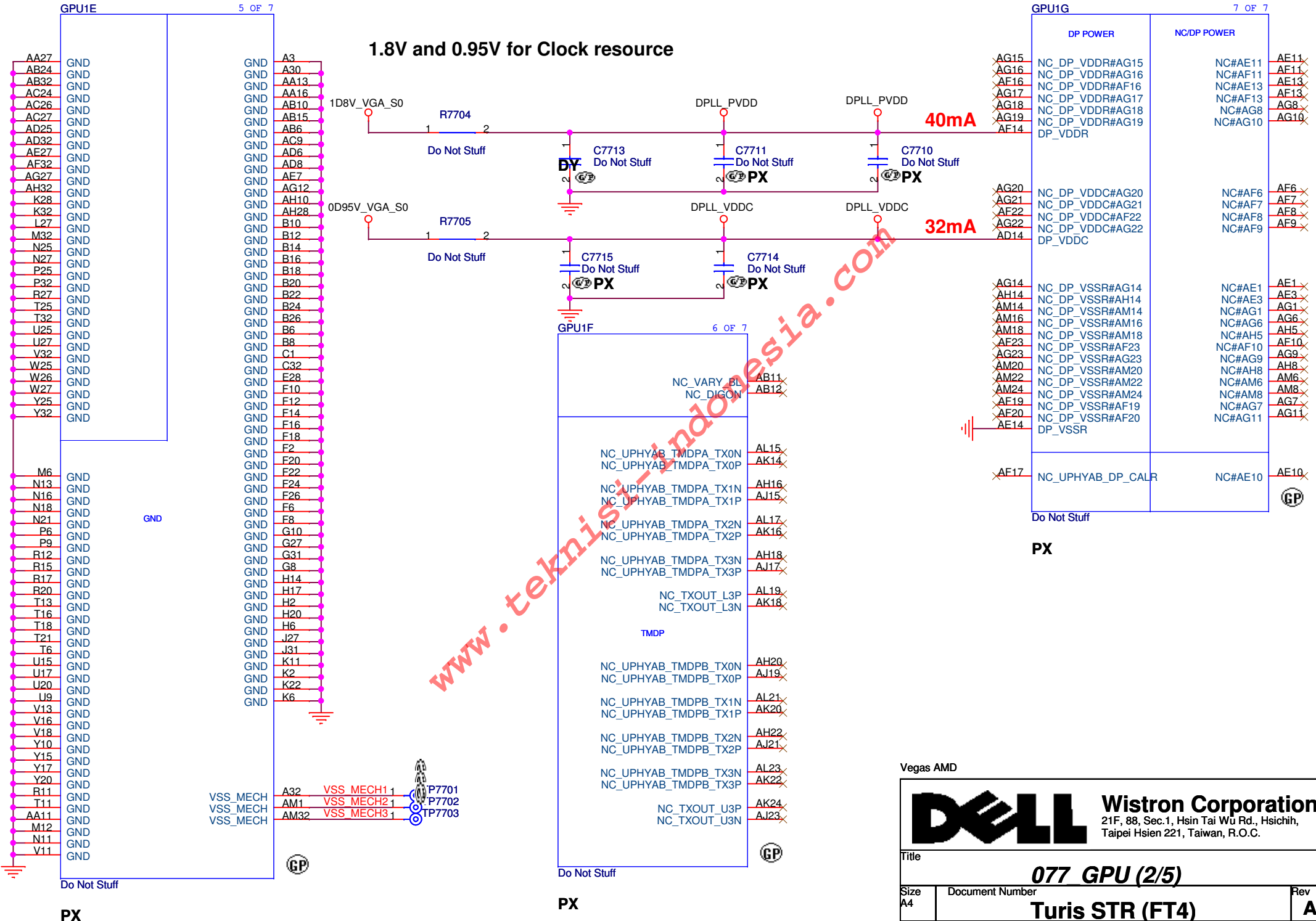
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1 OF 7


Pin Name	I/O	Description
PERSTB	I	Fundamental reset. 3.3-V tolerant pad. This signal must be asserted during any fundamental reset event, such as power up, warm boot, reset button pressed, CTL-ALT-DEL, Windows restart, or wake from D3.
PCIE_REFCLKP/N	I	PCI Express PLL differential reference clock (+/-). 100-MHz (\pm 300 ppm) input frequency; 0-V to 0.7-V single-ended swing.
PCIE_TX[7:0]P/N	O	PCI Express transmitter output data channel TX[7:0] (+/-). Differential serial data transmitted up to a 8.0-GT/s bit rate.
PCIE_RX[7:0]P/N	I	PCI Express receiver input data channel RX[7:0] (+/-). Differential serial data received up to a 8.0-GT/s bit rate.
PCIE_CALR_RX	I	Connect to PCIE_VDDC through a 1-k Ω (1% tolerance) resistor.
PCIE_CALR_TX	I	Connect to PCIE_VDDC through a 1.69-k Ω (1% tolerance) resistor.
CLKREQB	O	Reserved, do not connect on the PCB.



1.8V and 0.95V for Clock resource



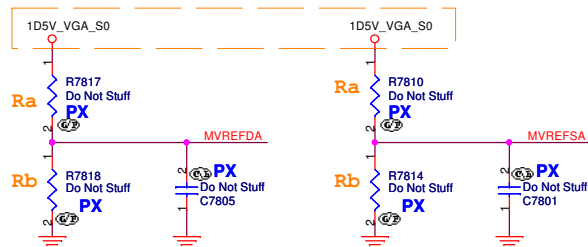
Vegas AMD

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
		Title: 077 GPU (2/5)	
Size: A4	Document Number: Turis STR (FT4)		Rev: A00
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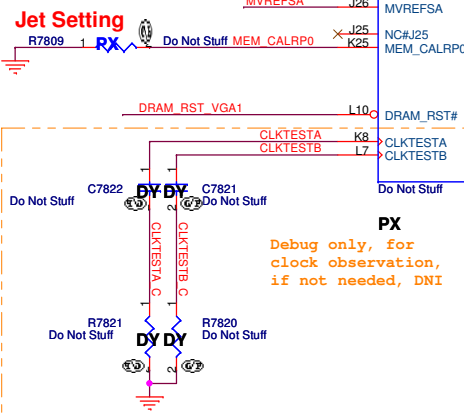
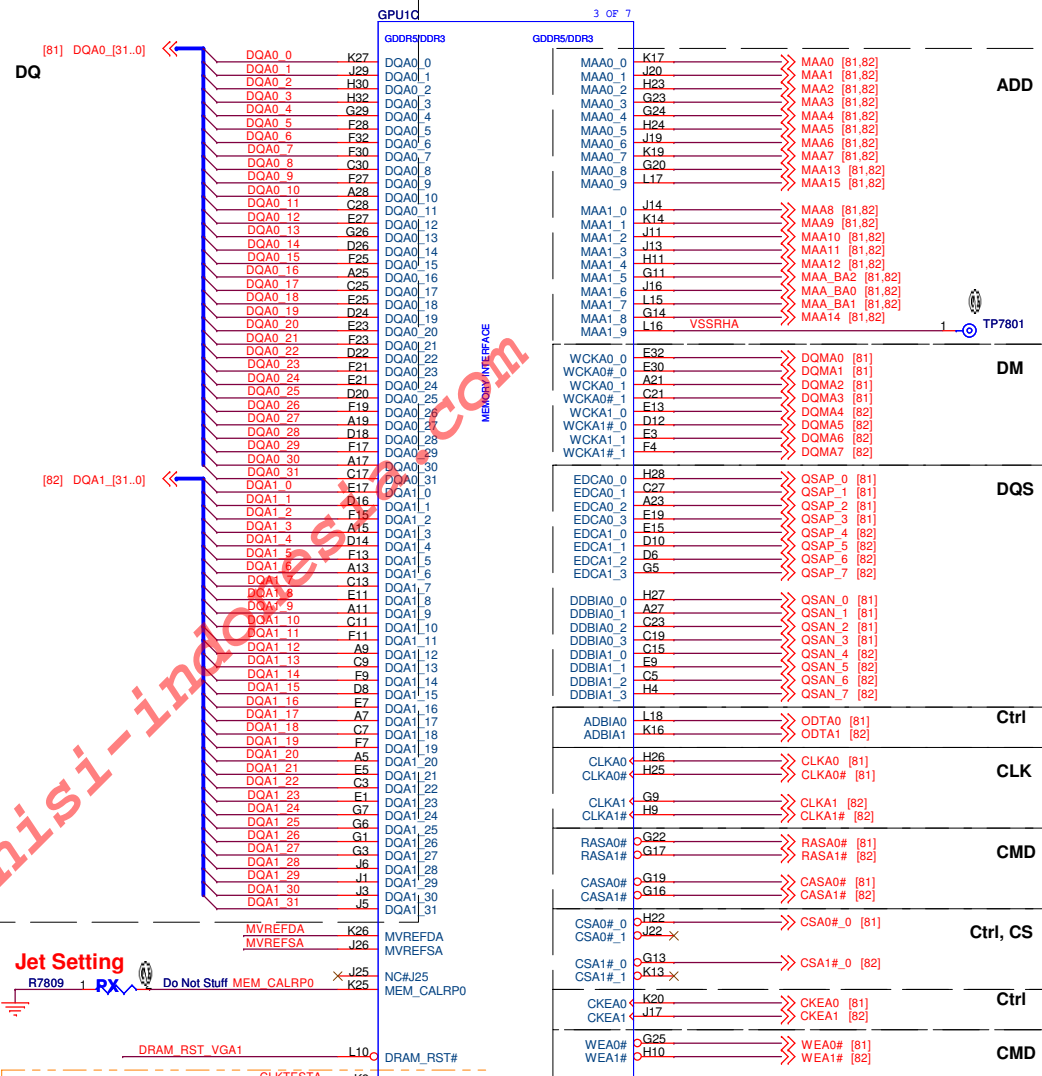
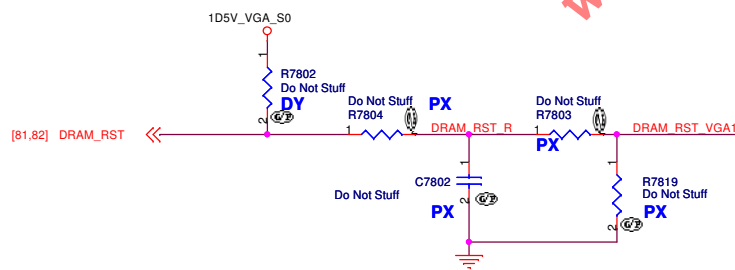
Please MVREF drivers and Caps close to ASIC

DDR3/GDDR3 Memory Stuff Option(JET/TOPAZ)

	GDDR5	GDDR3	DDR3
MVDDQ	1.5V	1D35V	1.5V
Ra	40.2R	40.2R	40.2R
Rb	100R	100R	100R



Place all these componets very close to GPU (within 25mm) and keep all components close to each other
This basic topology should be used for DRAM_RST for DDR3/GDDR5

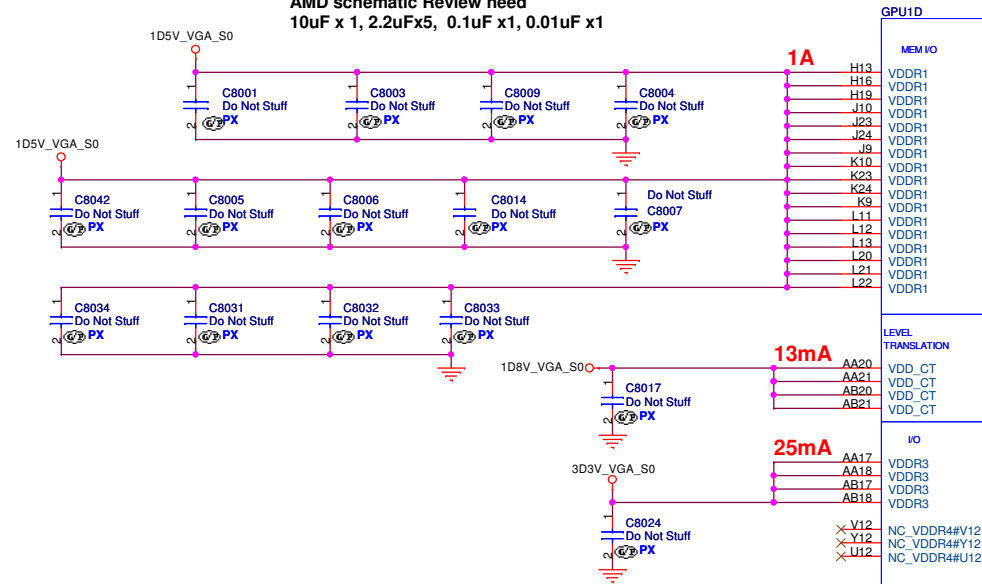


Vegas AMD

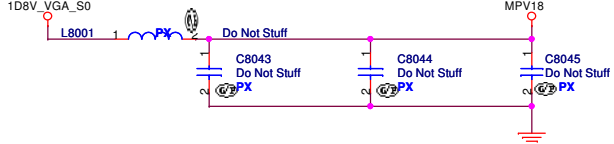


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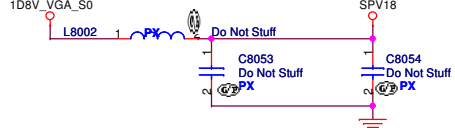
AMD schematic Review need
10uF x 1, 2.2uF x5, 0.1uF x1, 0.01uF x1



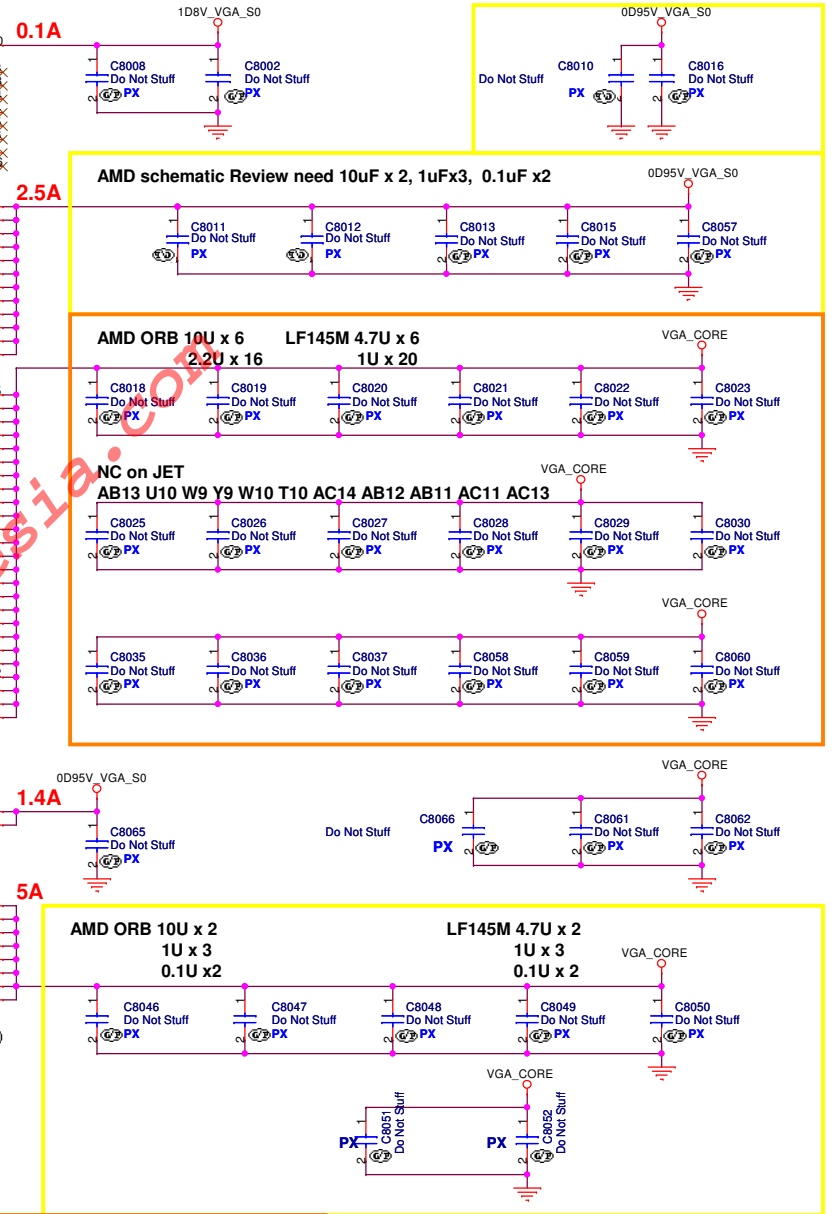
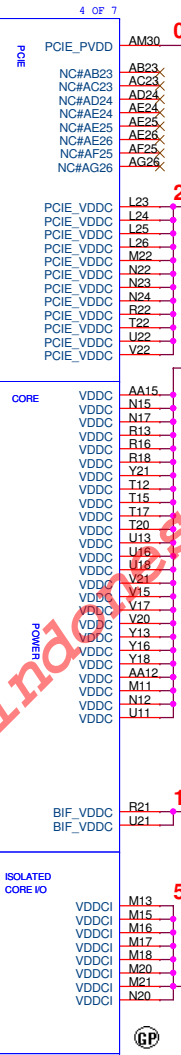
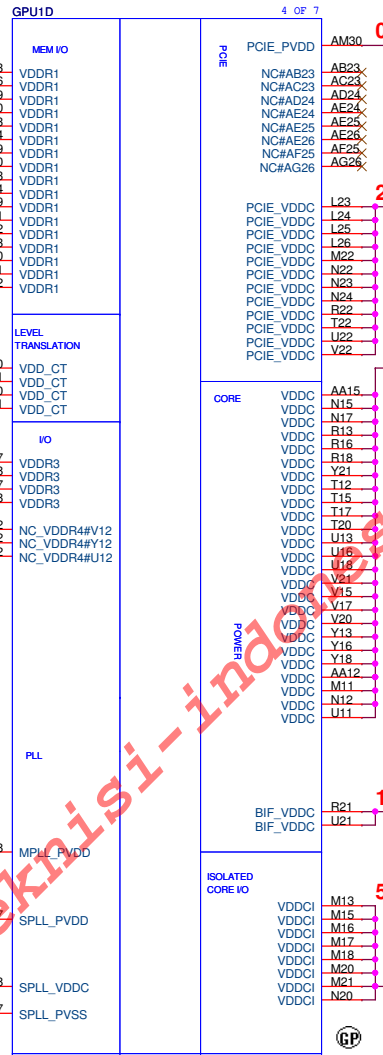
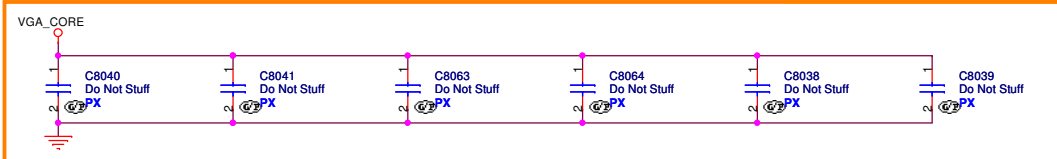
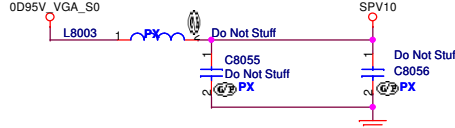
Memory phase lock loop power: Dedicated
analogue power in for memory PLLs



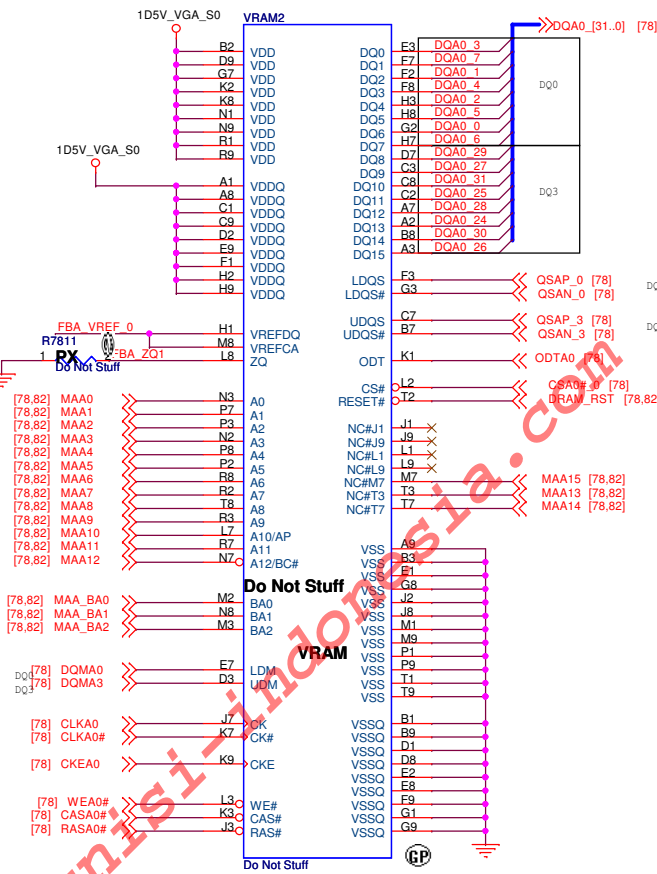
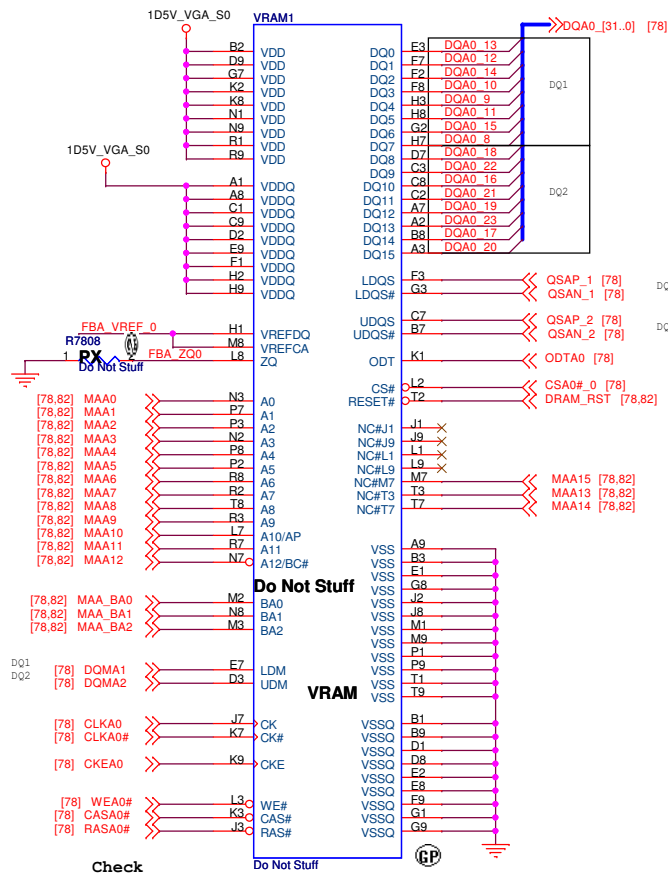
Engine phase loop power: Dedicated analogue
power pin for engine PLL



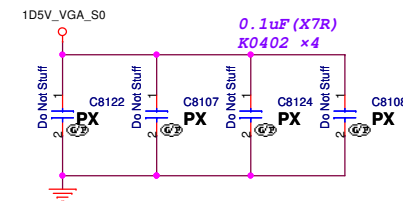
Engine phase loop power: Dedicated digital
power pin for engine PLL



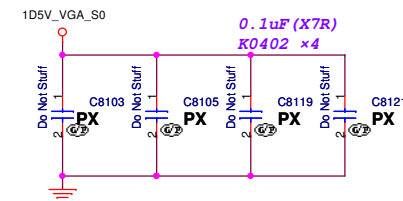
Main Func = Vram (DDR3L)



Place close VRAM1VDDQ ball

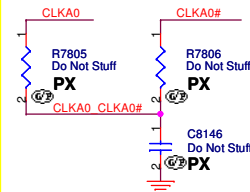


Place close VRAM2 VDD ball

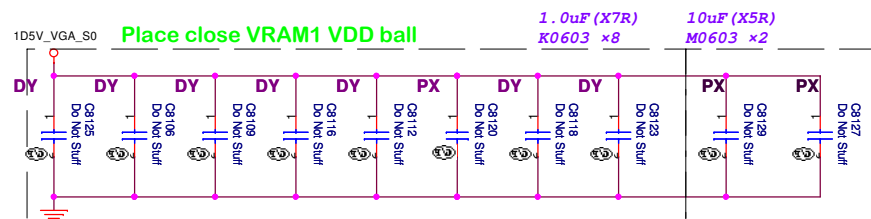
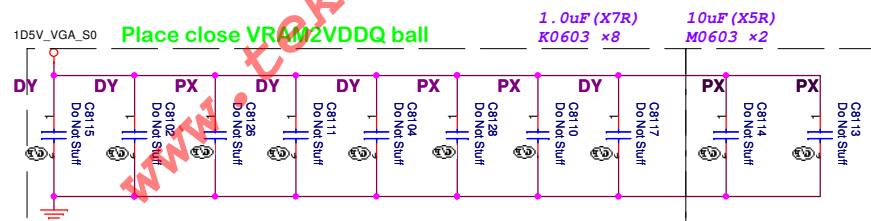
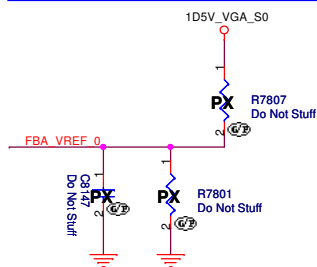


R7805 R7810

Single Rank, 40.2 Ohm = 64.40R25.6DL
Dual Rank, 80.6 Ohm = 64.80R65.6DL

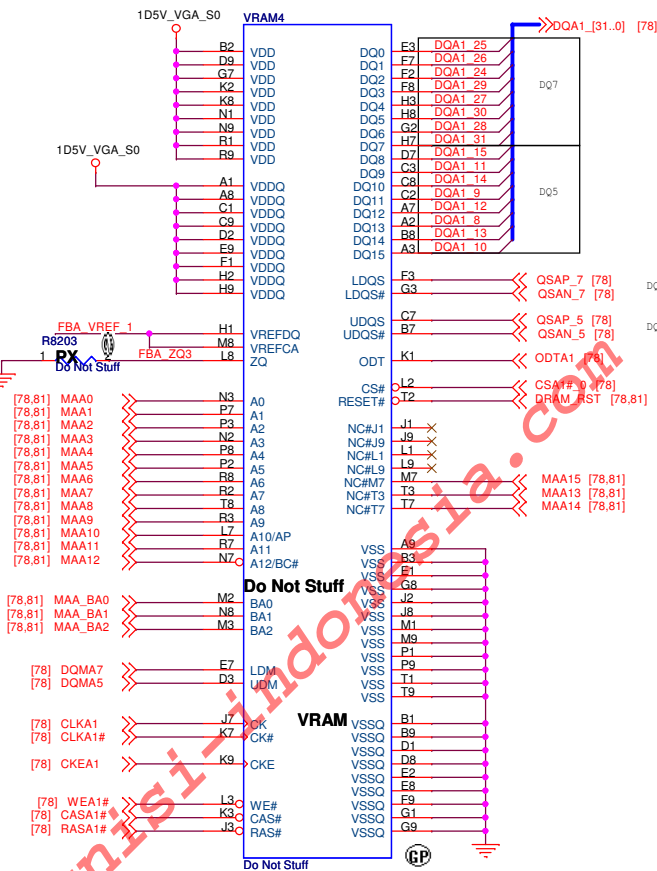
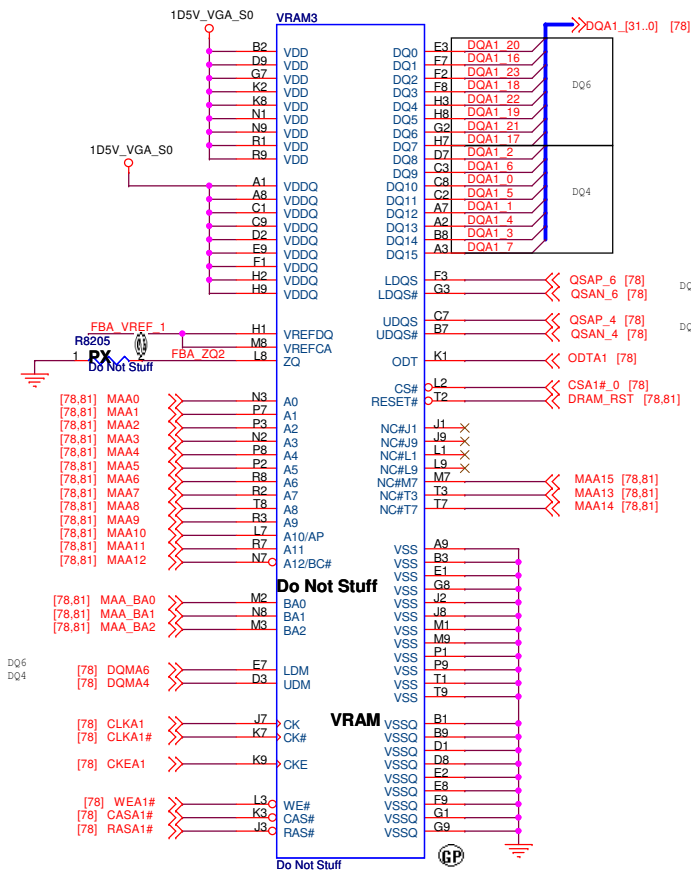


Frame Buffer Patition A-Lower Half

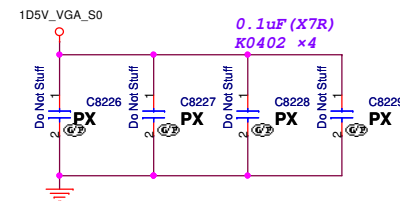


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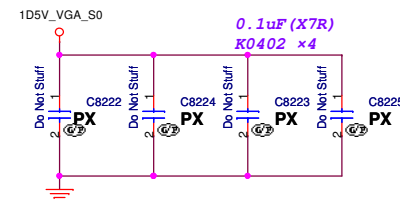
Main Func = Vram (DDR3L)



Place close VRAM3VDDQ ball

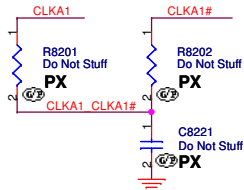


Place close VRAM4VDDQ ball

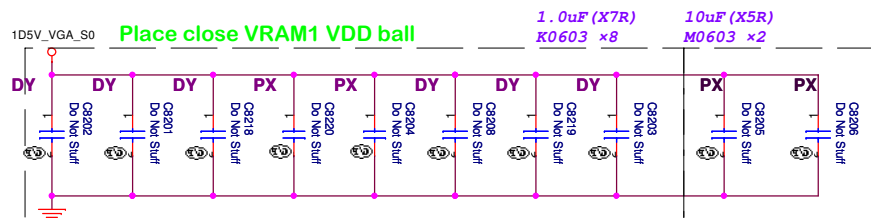
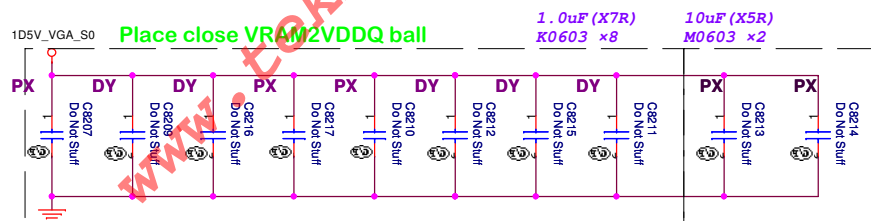
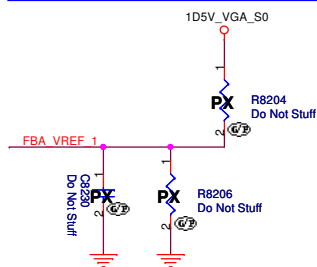


R7905 R7910

Single Rank, 40.2 Ohm = 64.40R25.6DL
Dual Rank, 80.6 Ohm = 64.80R65.6DL



Frame Buffer Patition A-Lower Half



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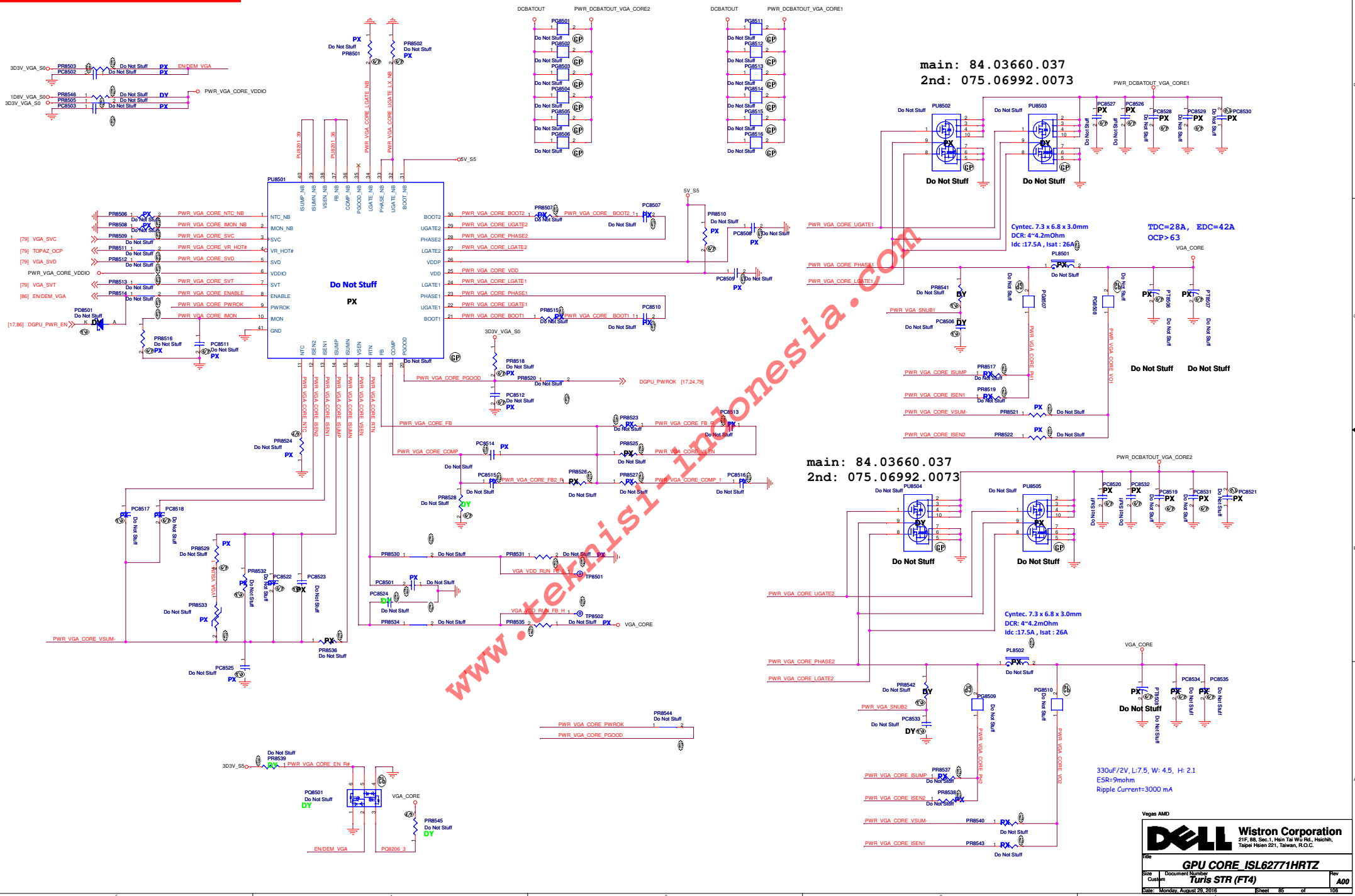
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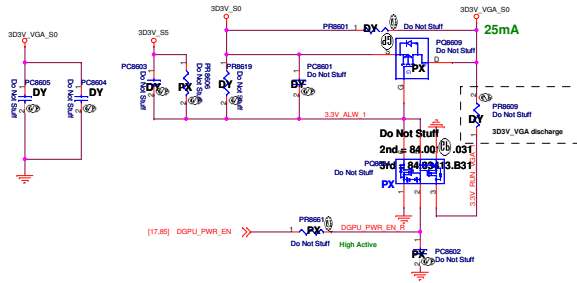
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Main Func = dGPU power



3D3V_S0 to 3D3V_VGA_S0 Transfer



GPU PWR Sequencing

3D3V_VGAS0

=> 0D95V_VGA_S0/1D8V_VGA_S0

=> 1D35V_VGA_S0

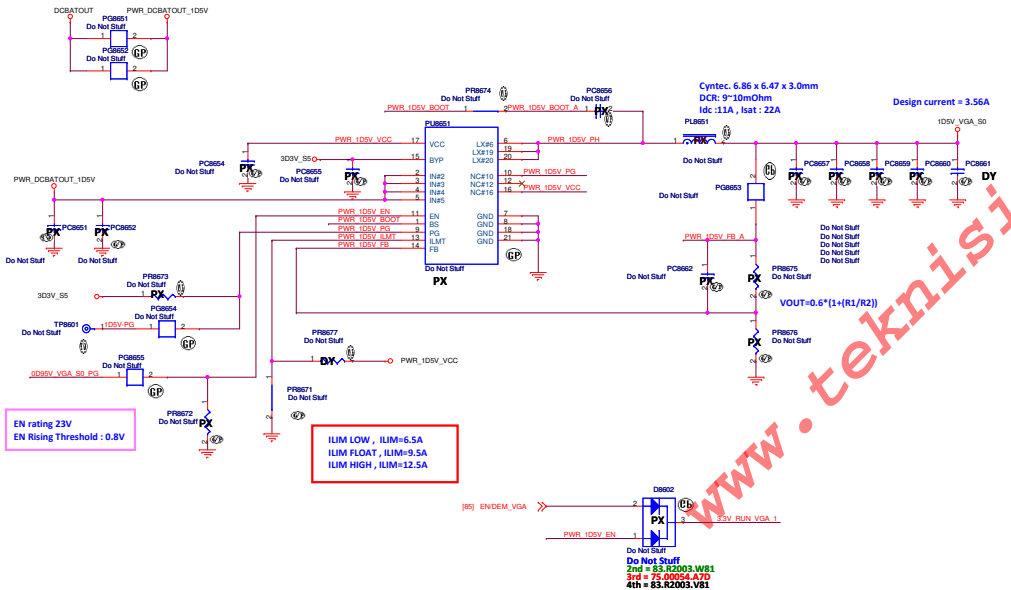
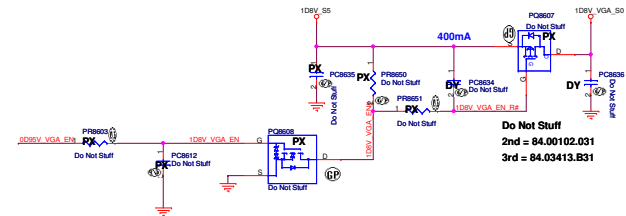
=> VGA_CORE

All the ASIC supplies must reach their respective nominal voltages within **20ms** of the start of the ramp-up sequence, though a shorter ramp-up duration is preferred. The maximum slew rate on all rails is 50mV/us.

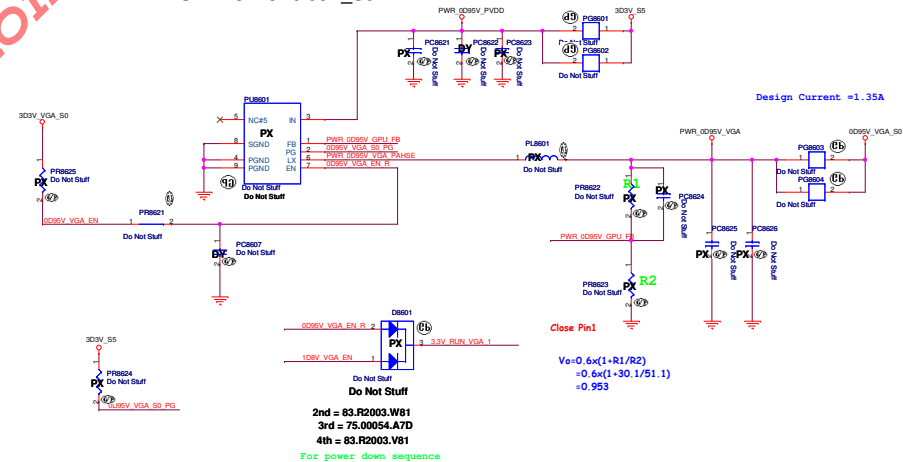
It is recommended that the 3.3V rail ramp up first.

It is recommended that the 0.95V rail reach at least 90% of its normal value no later than 2ms from the start of VDDC ramping up.

1D8V_VGA_S0




SYW232 for 0.95V_S5



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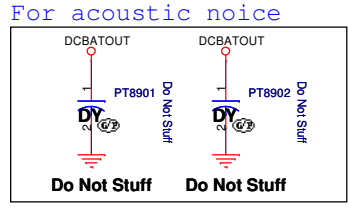
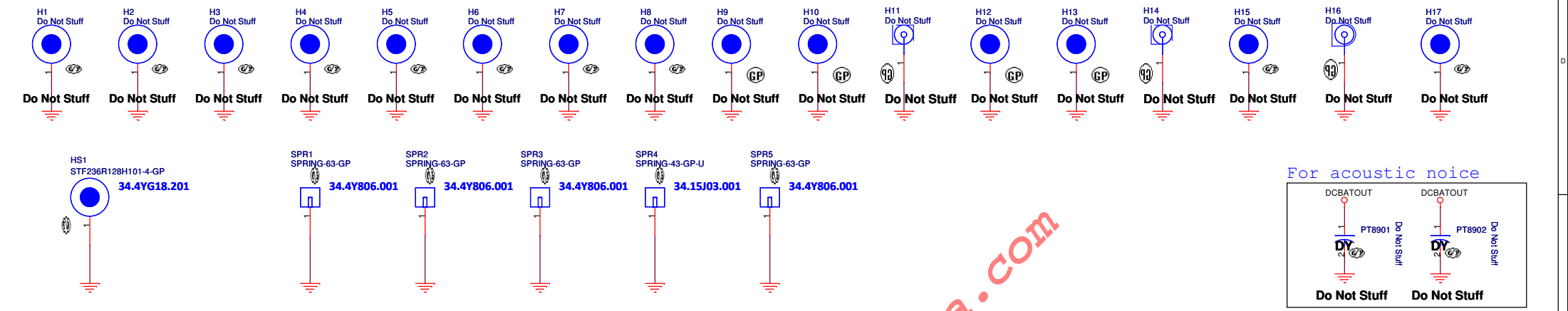
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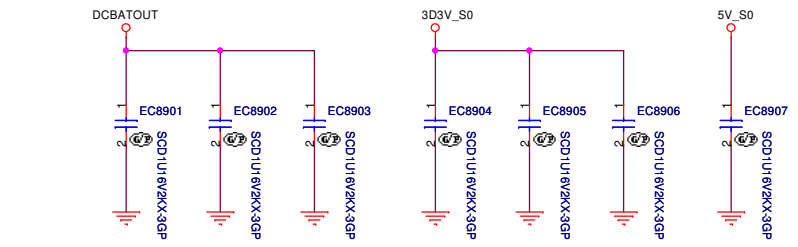
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Main Func = UnusedParts



Main Func = EMICapacitors




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004_ (Reserved)	040_Power Plane Enable&Sequence	076_GPU (1/5) PEG
005_CPU_ (DDR)	041_ (Reserved)	077_GPU (2/5) DIGITALOUT
006_ (Reserved)	042_DCIN JACK	078_GPU (3/5) VRAM I/F
007_CPU (VCC_CORE)	043_BATT CONN	079_GPU (4/5) GPIO/STRAP
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011_ (Reserved)	047_ISL62771_CPUCORE (2/2)	083_ (Reserved)
012_DDR4-SODIMM1	048_ (Reserved)	084_ (Reserved)
013_DDR4-SODIMM2	049_VDDCRGFX_APL5337	085_GPU CORE ISL62771HRTZ
014_ (Reserved)	050_ (Reserved)	086_DISCRETE VGAPOWER
015_ (Reserved)	051_ (Reserved)	087_ (Reserved)
016_CPU (CLK/SATA/USB/SPI/LPC)	052_APW8861_OD95V	088_ (Reserved)
017_CPU (ACPI/SD/AZ/GPIO/RTC)	053_VDDQ&VTT&VPP_APW8861	089_UNUSED PARTS/EMI Capacitors
018_ (Reserved)	054_DC2DC_1D8V/1D5V	090_ (Reserved)
019_ (Reserved)	055_LCD/Inverter CONN	091_ (Reserved)
020_CPU Strappings	056_VGA	092_ (Reserved)
021_ (Reserved)	057_HDMI	093_ (Reserved)
022_ (Reserved)	058_ (Reserved)	094_ (Reserved)
023_ (Reserved)	059_ (Reserved)	095_ (Reserved)
024_KBC Nuvoton NPCE985	060_SATA IF HDD/ODD	096_ (Reserved)
025_Flash (KBC+PCH) /RTC	061_NGFF_WLAN CONN	097_ (Reserved)
026_THERMAL NCT7718W/Fan	062_ (Reserved)	098_ (Reserved)
027_Audio Codec ALC3234	063_ (Reserved)	099_ (Reserved)
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Change notes

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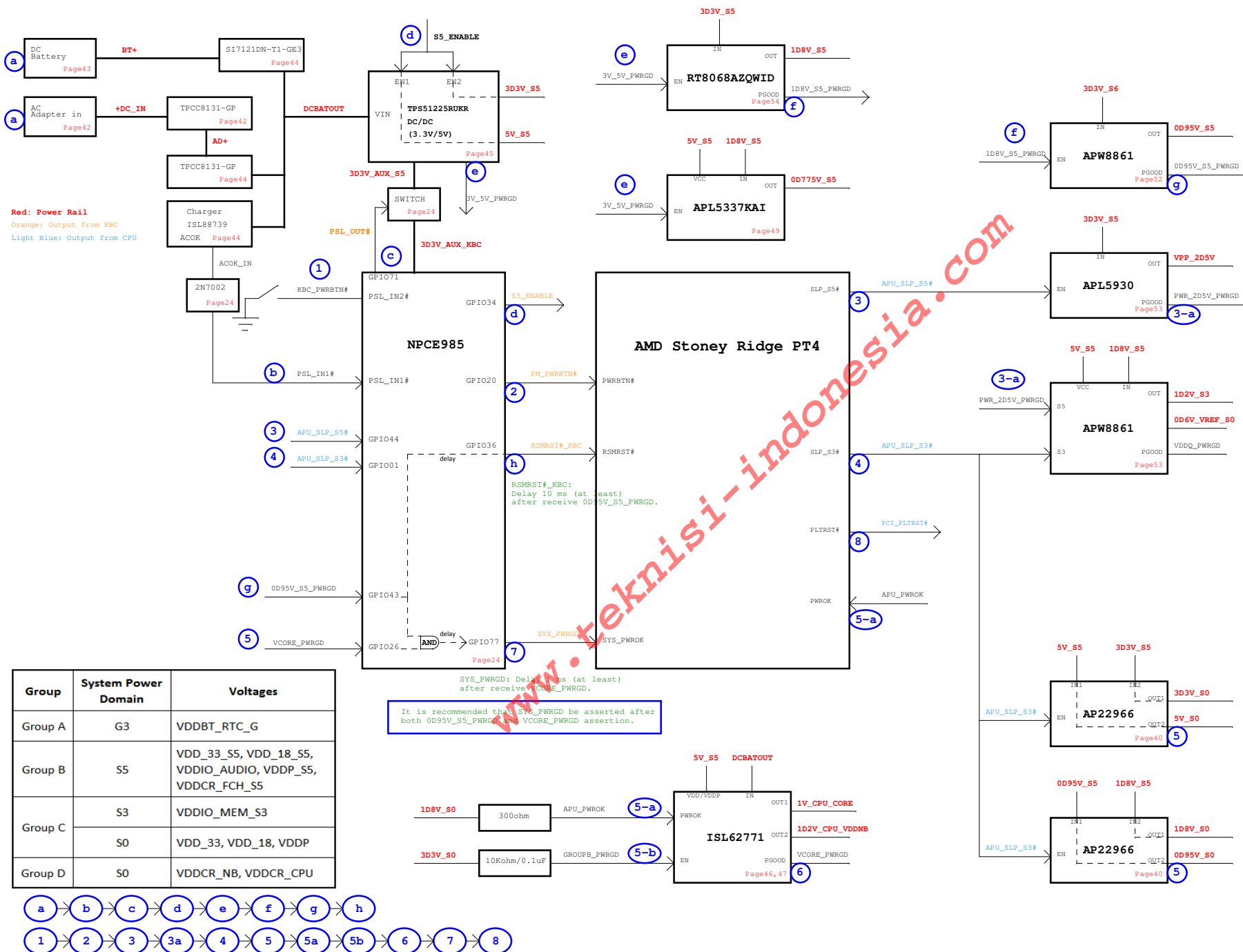
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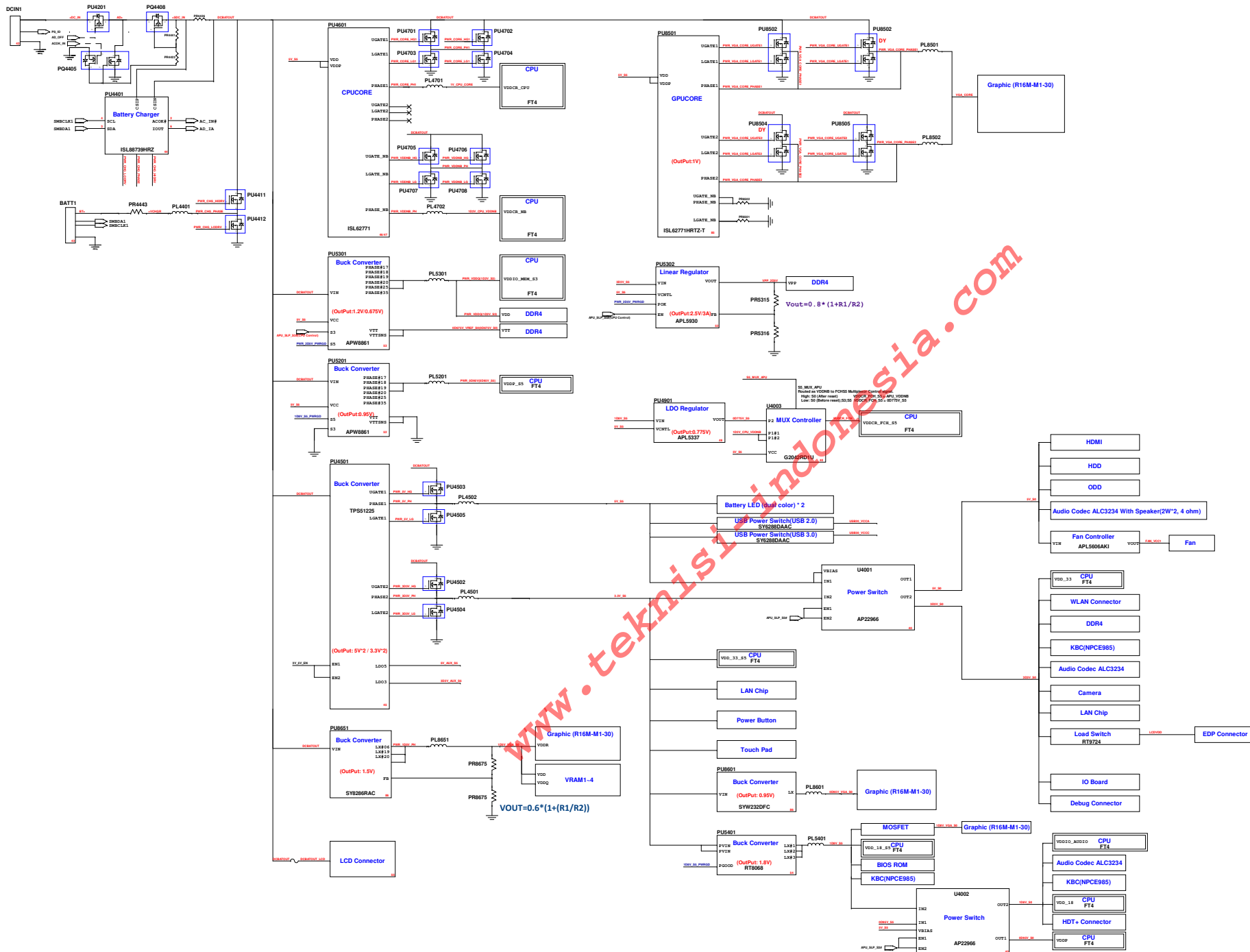
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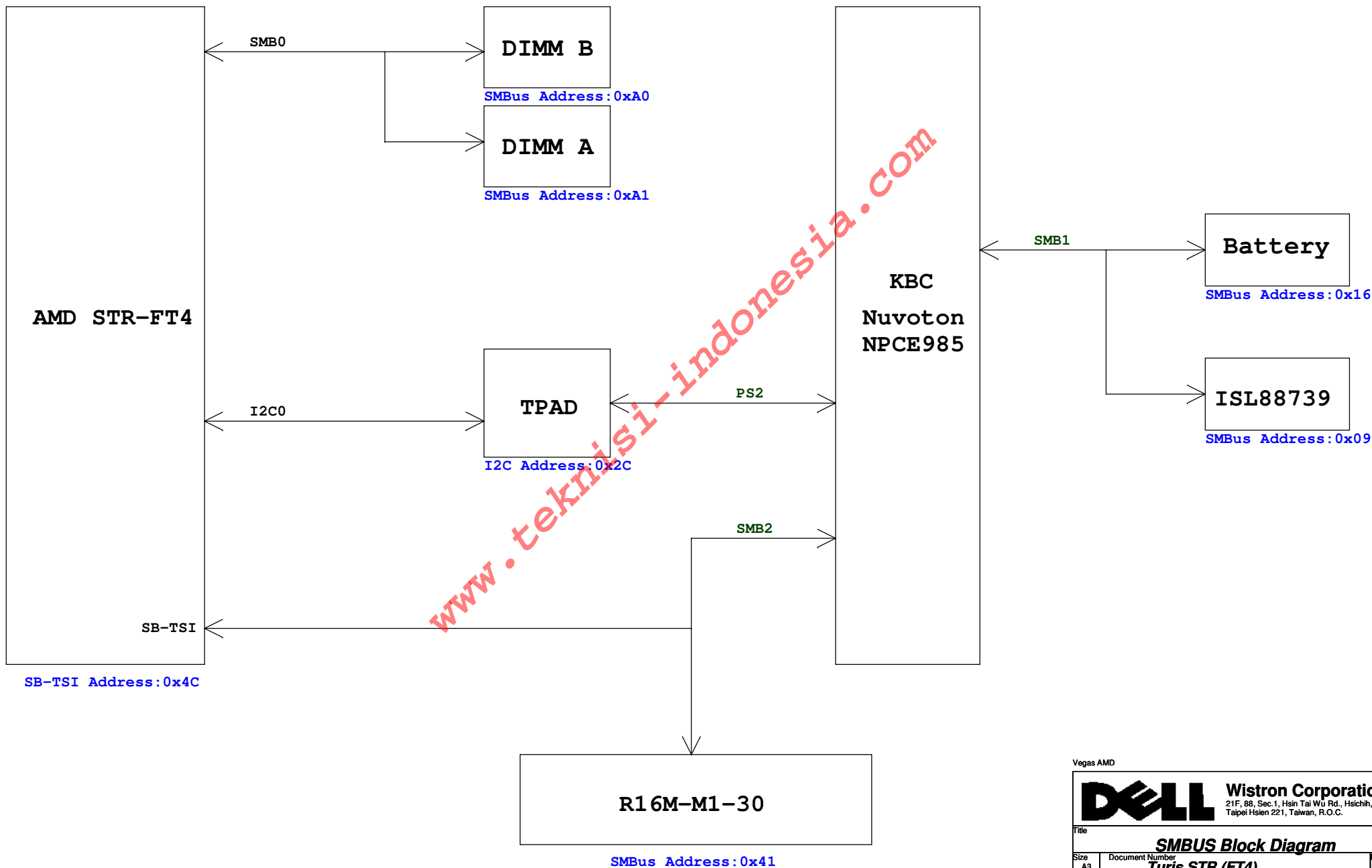
Vegas STR-FT4 POWER UP SEQUENCE DIAGRAM



Group	System Power Domain	Voltages
Group A	G3	VDDBT_RTC_G
Group B	S5	VDD_33_S5, VDD_18_S5, VDDIO_AUDIO, VDDP_S5, VDDCR_FCH_S5
Group C	S3	VDDIO_MEM_S3
	S0	VDD_33, VDD_18, VDDP
Group D	S0	VDDCR_NB, VDDCR_CPU

【Turis AMD Power Block Diagram】





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SMBUS Block Diagram

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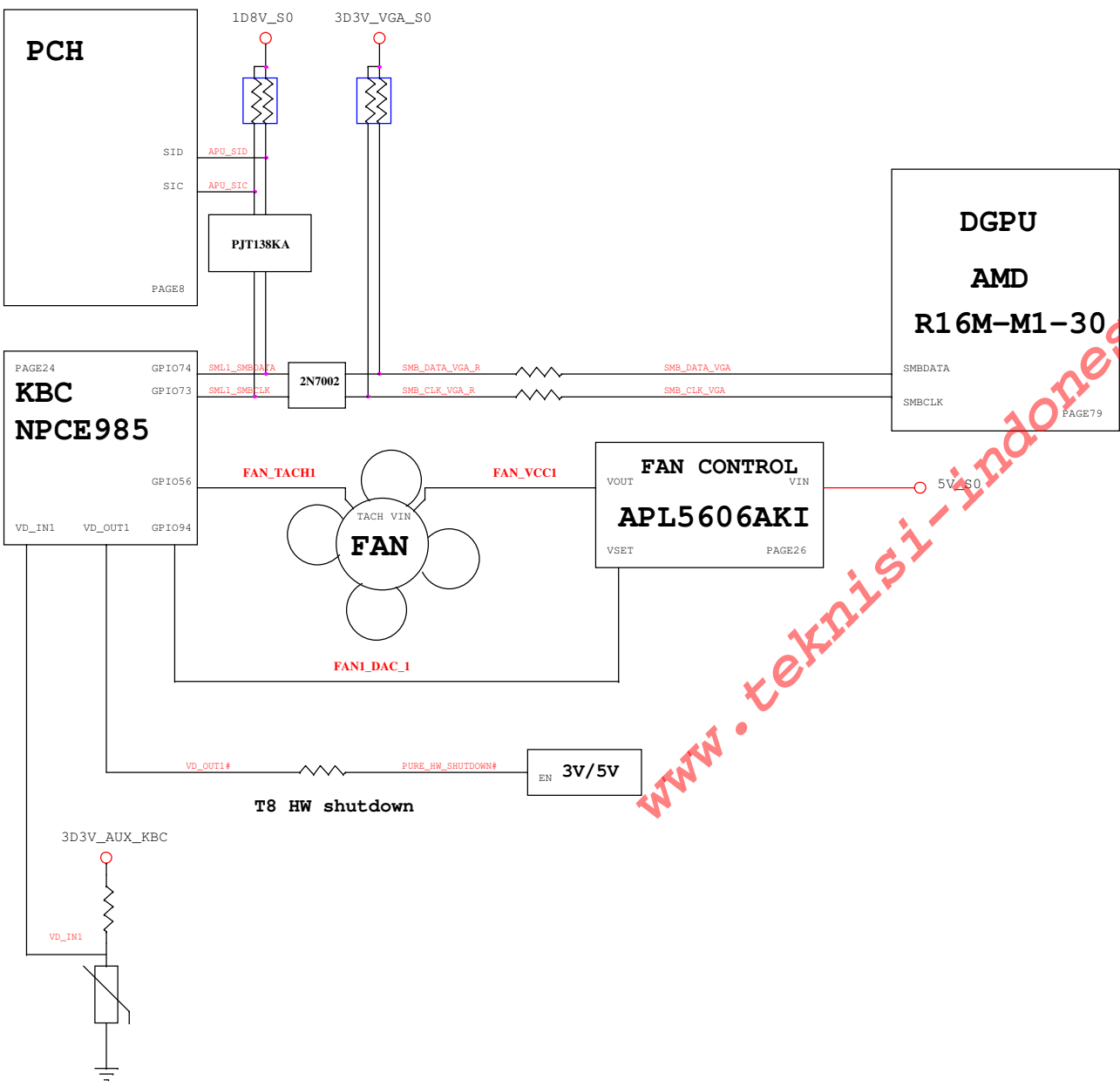
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Thermal Block Diagram



Audio Block Diagram

